



03036279

PE  
6-30-03

ARLS

expanding our focus

FILE PROCESSING  
NOV 09 2003  
SECTION

PROCESSED

NOV 04 2003

THOMSON  
FINANCIAL

annual report 2003

complete interface solutions

ders:



.....◆ Focused aggressively and successfully on increasing revenue from China with support from our offices in Hong Kong, Shanghai, Beijing and Shenzhen.

.....◆ Maintained a solid cash balance of \$149 million, with no debt, despite operating in a severe economic environment. Our strong balance sheet provides financial flexibility.

.....◆ Reduced our pro forma EPS loss a penny per quarter from (\$0.04) in the first quarter to (\$0.01) in the fourth quarter, despite reduced business levels.

.....◆ In a very difficult pricing environment, we were able to keep our margins virtually flat throughout the year.

.....◆ Emphasized disciplined control to mitigate the financial and operating risks inherent in our business without compromising innovation.

.....◆ Increased our patent portfolio to 54 US patents granted with 26 pending.

.....◆ Provided superior quality and service resulting in receipt of our third consecutive "Partners in Performance" global supplier award from Celestica for being a key supplier and business partner.

### Our Expanding Product Portfolio

#### siliconCLOCK & TIMING:

.....◆ Brought to market 15 new higher margin clock products. This includes an industry leading performance PLL for DDR400 DIMM, and a new Translator family that converts signals from single-ended to LVPECL. Announced the acquisition of SaRonix, a company with Crystal, Oscillator, VCXO, TCXO, and other Frequency Translator products.

#### siliconINTERFACE LOGIC:

.....◆ First to market with SSTVF high-performance registered buffer logic for DDR333 and DDR400. First to market with 10 LVTC lower power products. Delivered the industry's fastest TTL Gate Logic family to enhance our voltage translation portfolio. Added 18 new LVC high-speed and low-cost devices.

#### siliconCONNECT:

.....◆ Expanded PCI Bridge portfolio by introducing 3 new Intel compatible devices. Complemented our LVDS family with 20 new devices that range from a single receiver in a SOTiny package to a 10-bit wide driver with high drive capability. Released a Gigabit SerDes transceiver

#### siliconSWITCH:

.....◆ Introduced the worlds first Gigabit Ethernet LAN Switch. Began sampling an industry first USB 2.0 switching solution. Released a digital bus switch that allows notebooks

to easily expand their memory slots. Delivered a new family of ultra-low 0.7-Ohm Analog Switches. Led the industry in providing the worlds smallest no-lead TDFN SOTiny™ packages.

### Corporate Governance

.....◆ We have always been committed to providing reliable information and being straight-forward with our investors. With passage of the Sarbanes-Oxley Act of 2002, we have taken further steps to ensure that we remain compliant. Ongoing education and involvement of Directors, Officers, key employees and input from our outside counsel and auditors have all contributed to establishing an environment that upholds the spirit of the legislation and our commitment to provide quality information.

### A New Year—Looking Ahead

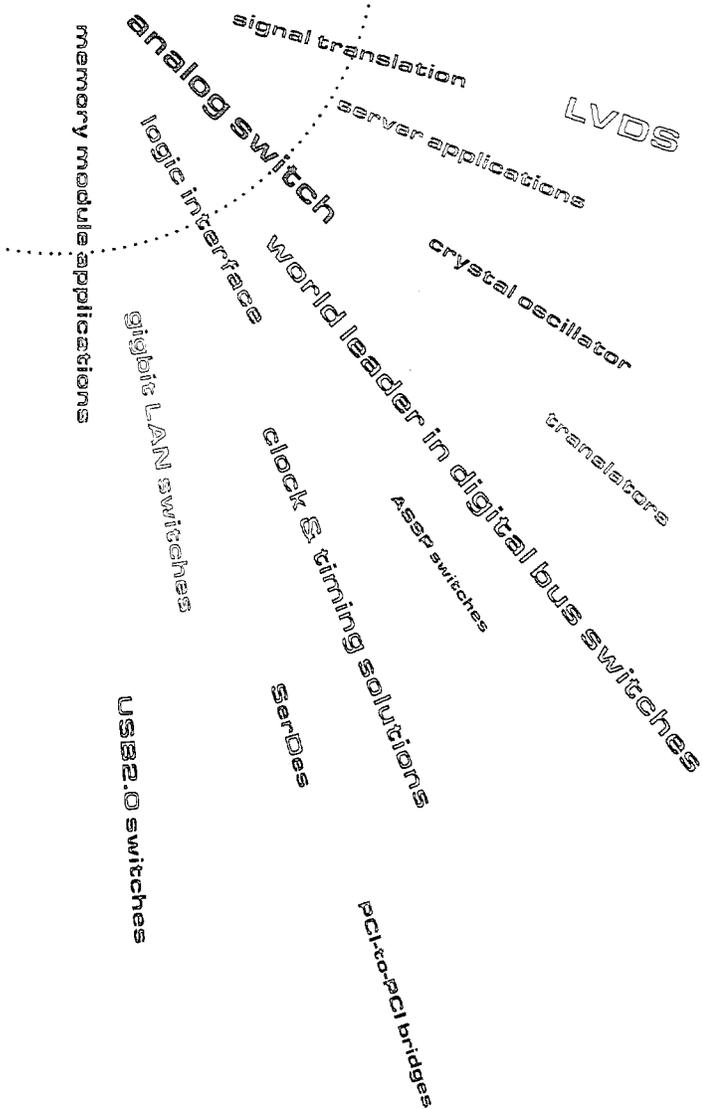
.....◆ We are quite optimistic regarding the future in spite of the short-term challenges that we face. We have the resources available to execute our strategic and operating plans effectively while leveraging our core competencies into long-term corporate advantages. We continue to globally invest in new products and technologies, achieving numerous design wins, and expect to see the result of these successes become evident during the industry upturn.

.....◆ I would like to thank our customers, suppliers, and investors for their support, as well as our employees for their dedication and hard work. I believe the best is yet to come.

Thank you,

Alex Hui,  
President, CEO, Chairman of the Board  
Pericom Semiconductor Corporation

# To our Sharehol



..... It would be easy to dwell on the negative aspects of the semiconductor industry during fiscal 2003 as the downturn has been long and deep. However, I feel it is more appropriate to let you know that our optimism has not been extinguished, and we are preparing for a brighter future. We have consistently managed Pericom throughout this downturn to some fundamental guiding principles that include continuing our heavy investment in R&D to expand our focus product lines, building lower-cost infrastructure and capabilities to leverage our strengths and relationships with our customers in Asia, outsourcing operations, investing strategically to grow the business, and conserving our working capital with tight fiscal controls.

..... We remain confident that our ongoing focus and commitment to leveraging our core competencies has positioned us for an aggressive and successful response when our industry enters a sustainable recovery from this lengthy down cycle.

## Responding to the Marketplace

..... Introduced a total of 77 new products across all product lines. Particular emphasis was placed on products that offer advanced technology at a lower cost of ownership, while delivering solutions essential to support the architectural desires of our customers.

..... Continued a successful product mix shift towards our higher margin focus families of Clock/Timing, Analog Switch, PCI Bridge, LVDS, and Application Specific devices.

..... Pericom Taiwan Limited (PTL), a wholly owned subsidiary, was established this year. PTL is located in close proximity to a number of our customers, offering the unique opportunity to work with them practically on-site.

..... Pericom Technology Inc (PTI), our 44% owned sister company in China, made significant strides in fiscal 2003 growing 55% year over year.

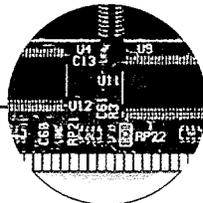
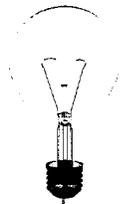
..... Announced the pending acquisition of the assets of SaRonix LLC, a frequency timing products (FTP) company based in Menlo Park, CA. FTP is a key focus for Pericom. Our combined entity will be the first in the industry to provide a complete timing solution with quartz plus silicon based products. The acquisition, expected to close in October 2003 and priced at approximately \$10 million, is expected to add \$18-\$20 million in revenue in calendar 2004.

..... Invested in GCT Semiconductor, a private company that designs and develops wireless communications technology. This supports our entry into the fast growing wireless markets with exclusive rights to address many emerging opportunities in consumer applications.

# Providing Solutions

Our Expanding Portfolio

With increased investment in R&D to expand our key product lines, we can focus and leverage our core competencies for more competitive solutions.



Pericom has sales, support, and design centers worldwide. Our swift response to market requirements combined with advanced product design, manufacturing and packaging capabilities has enabled the company

to establish itself as a market and technology leader in the interface IC segment. Our products

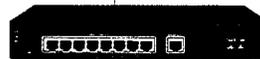
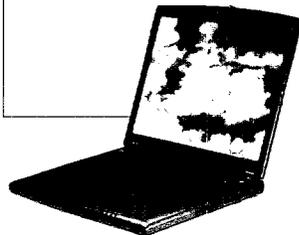
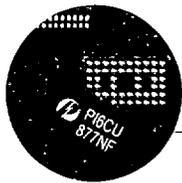
are essential for full utilization of the speed and bandwidth built into today's Microprocessors, Memory IC's, LAN's,

and WAN's.

# Responsive Growth

Shifting our Focus

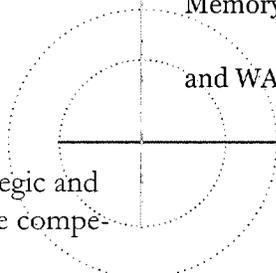
Our emphasis is on products offering advanced technology at a lower cost to our customer, we deliver solutions essential to support the architectural desires of our customers.



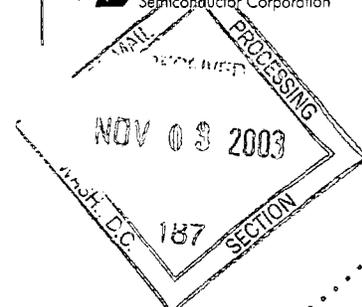
# Looking Forward

Strategic Advantages

We have the resources available to execute our strategic and operating plans effectively, while leveraging our core competencies into long-term corporate advantages.



## financial highlights 2003



(Dollar and share amounts in thousands, except per share amounts)

For the year:	fiscal year ended june 30		
	2003	2002	Change
Net revenues	\$44,958	\$47,589	(5.5%)
Operating loss	(10,571)	(9,723)	8.7%
Net loss	(4,327)	(5,267)	(17.8%)
Basic & diluted loss per share:	(\$0.17)	(\$0.21)	(19.0%)
Shares used in computing loss per share:	25,721	25,339	1.5%
At year-end:			
Cash & short term investments	\$148,990	\$155,812	(4.4%)
Total assets	190,237	193,745	(1.8%)
Shareholders' equity	182,323	185,722	(1.8%)

**SECURITIES AND EXCHANGE COMMISSION**  
**WASHINGTON, D.C. 20549**  
**FORM 10-K**

(Mark One)

- Annual report pursuant to section 13 or 15(d) of the Securities and Exchange Act of 1934 for the fiscal year ended June 28, 2003.
- Transition report pursuant to section 13 or 15(d) of the Securities and Exchange Act of 1934 for the Transition period from \_\_\_\_\_ to \_\_\_\_\_

**Commission File Number 0-27026**

**Pericom Semiconductor Corporation**  
(Exact Name of Registrant as Specified in Its Charter)

California  
(State or Other Jurisdiction of  
Incorporation or Organization)

77-0254621  
(I.R.S. Employer  
Identification No.)

2380 Bering Drive  
San Jose, California 95131  
(Address of Principal Executive Offices)

95131  
(Zip Code)

Registrant's Telephone Number, Including Area Code: (408) 435-0800

Securities registered pursuant to Section 12(b) of the Act: None

Securities registered pursuant to Section 12(g) of the Act: Common Stock

Indicate by check mark whether the registrant: (1) has filed all reports required to be filed by Section 13 or 15(d) of the Securities Exchange Act of 1934 during the preceding 12 months (or for such shorter period that the Registrant was required to file such reports), and (2) has been subject to such filing requirements for the past 90 days.

Yes  No

Indicate by check mark if disclosures of delinquent filers pursuant to Item 405 of Regulation S-K is not contained herein, and will not be contained, to the best of registrant's knowledge, in definitive proxy or information statements incorporated by reference in Part III of this Form 10-K or any amendment to this Form 10-K.

Indicate by check mark whether the Registrant is an accelerated filer (as defined in Exchange Act Rule 12b-2):

Yes  No

The aggregate market value of voting stock held by non-affiliates of the Registrant, based on the closing price of the Common Stock on September 23, 2003 as reported by the Nasdaq National Market was approximately \$247,973,000. Shares of common stock held by each officer and director have been excluded in that such persons may be deemed to be affiliates. This determination of affiliate status is not necessarily a conclusive determination for other purposes.

As of September 23, 2003 the Registrant had outstanding 25,865,793 shares of Common Stock.

## **DOCUMENTS INCORPORATED BY REFERENCE**

Parts of the Registrant's Proxy Statement for the Annual Meeting of Shareholders to be held December 17, 2003, which will be filed subsequently, are incorporated by reference in Part III of this report on Form 10-K.

Pericom Semiconductor Corporation  
Form 10-K for the Year Ended June 28, 2003

INDEX

	<u>Page</u>
<b>PART I</b>	
Item 1: Business	4
Item 2: Properties	22
Item 3: Legal Proceedings	22
Item 4: Submission of Matters to a Vote of Security Holders	22
<b>PART II</b>	
Item 5: Market for Registrant's Common Stock and Related Stockholder Matters	23
Item 6: Selected Financial Data	24
Item 7: Management's Discussion and Analysis of Financial Condition and Results of Operations	25
Item 7A: Quantitative and Qualitative Disclosures about Market Risk	32
Item 8: Financial Statements and Supplementary Data	33
Item 9: Changes in and Disagreements with Accountants on Accounting and Financial Disclosure	34
Item 9A: Controls and Procedures	34
<b>PART III</b>	
Item 10: Directors and Executive Officers of the Registrant	35
Item 11: Executive Compensation	35
Item 12: Security Ownership of Certain Beneficial Owners and Management	35
Item 13: Certain Relationships and Related Transactions	35
Item 14: Principal Accountant Fees and Services	35
<b>PART IV</b>	
Item 15: Exhibits, Financial Statement Schedules and Reports on Form 8-K	36
Signatures	58

## PART I

### ITEM 1. BUSINESS

We design, develop and market high-performance interface integrated circuits, or ICs, used in many of today's advanced electronic systems. Interface ICs, such as interface logic, switches and timing management products, transfer, route and time electrical signals among a system's microprocessor, memory and various peripherals and between interconnected systems. Our interface products increase system bandwidth in applications such as notebook computers, servers, network switches and routers, storage area networks and wireless base stations. We offer products that increase bandwidth by widening the data path, or bus (enlarging the pipe), increasing the data rate (increasing the flow rate through the pipe), or allowing multiple, simultaneous transactions on the bus.

We have combined our extensive design technology and applications knowledge with our responsiveness to the specific needs of electronic systems developers to become a leading supplier of high-performance interface ICs. We have evolved from one product line in fiscal 1992 to four developed and growing product lines--SiliconSwitch™, SiliconInterface™ Logic, SiliconClock™ and SiliconConnect™--aimed at providing an increasing breadth of interface IC solutions to our current and target customers. We have a comprehensive portfolio of products including 77 new products that were introduced during fiscal year 2003. Our worldwide customers include leading OEMs, contract manufacturers and distributors.

Our website address is <http://www.pericom.com>. Our annual reports on Form 10-K, quarterly reports on Form 10-Q, current reports on Form 8-K and all amendments to those reports, and the proxy statement for our annual meeting of shareholders are all available, free of charge, on our website as soon as practicable after we file the reports with the Securities and Exchange Commission.

### INDUSTRY BACKGROUND

#### *OVERVIEW*

The presence of electronic systems and subsystems permeates our everyday life, as evidenced by the growth of the personal computer, mobile communications, networking and consumer electronics markets. The growth of these markets has been driven by systems characterized by ever-improving performance, flexibility, reliability and multi-functionality, as well as decreasing size, weight and power consumption. Advances in ICs through improvements in semiconductor technology have contributed significantly to the increased performance of, and demand for, electronic systems and to the increasing representation of ICs as a proportion of overall system cost. This technological progress has occurred at an accelerating pace, while the cost of electronic systems has remained steady or declined.

The development of high-performance personal computers, the requirement for higher network performance and the increased level of connectivity among different types of electronic devices have driven the demand for high-speed, high-performance interface circuits to handle the transfer, routing and timing of digital and analog signals at high speeds with minimal loss of signal quality. High-speed signal transfer is essential to fully utilize the speed and bandwidth of the microprocessor, the memory and the LAN or WAN. High signal quality is equally essential to achieve optimal balance between high data transmission rates and reliable system operation. Without high signal quality, transmission errors occur as bandwidth increases. Market requirements for interface circuits are driven by the same market pressures as those imposed on microprocessors, including higher speed, reduced power consumption, lower-voltage operation, smaller size and higher levels of integration.

Our interface products serve to increase system bandwidth in applications such as servers, network switches and routers, storage area networks, wireless basestations, and notebook computers. Bandwidth can be increased by *widening the datapath (widening the pipe)*, *increasing the clock rate (increase the flowrate through the pipe)* or allowing multiple, simultaneous transactions on the bus using a crossbar switch. We are pioneering technology in each of these areas.

The problems associated with signal quality that must be addressed by the interface ICs are magnified by increases in the speeds at which interface ICs must transfer, route and time electrical signals, the number of interconnected devices that send or receive signals and the variety of types of signals processed by the interface ICs. The most significant performance challenges faced by designers of interface ICs are the requirements to transfer signals at high speed with low propagation delay, minimize signal degradation such as "noise," "jitter," and "skew" and reduce electromagnetic interference ("EMI"). Minimizing propagation delay, sources of signal degradation and interference is needed to enable today's state-of-the-art electronic systems to function reliably.

We believe that several major market trends make reliable operations of systems at high frequency and high data transfer rates *critical*. *Internet and high-performance network applications continue to push for more data bandwidth on system buses and across system boundaries*. Computer and networking system clock frequencies continue to increase at a very rapid rate, shortening the time available to perform data transfers. While the data transfer rate has typically increased every few years, the continuing desire for higher system reliability with minimal system downtime creates increasing pressure to achieve lower data error rates. Increasing system-wide EMI emissions resulting from higher-frequency ICs compels system designers to develop and implement new ways to further reduce these emissions. These factors all increase the need for very high-speed interface circuits with outstanding performance specifications.

With processing power continuing to double every 18 months (Moore's law) the speed at which microprocessors can access memory becomes the system bandwidth constraint in high-speed computing. We have developed solutions with market leaders to support higher speed processor-memory interfacing to support the Double Data Rate ("DDR" and "DDR II") SDRAM, both on the system motherboard and memory modules.

In server applications, we support higher system bandwidth through the use of bus switches to isolate the system's memory modules from the bus when they are not being addressed. Our interface products are also used to enable live insertion of PCI boards ("hot swapping"). This prevents system downtime when boards need to be replaced. We have similar products for hot-docking notebook computers and our products are used in the majority of the notebook computers on the market today.

In high-bandwidth systems data transfer needs to be synchronized, creating a high demand for timing products. Our timing products provide the precise timing signals needed to ensure reliable data transfer at high speeds in applications ranging from notebook computers to network switches. As systems continue to grow in processing power and complexity the demand for these products will accelerate. The demand for higher precision will also *continue to increase as timing margins shrink in higher bandwidth systems*.

Electronic systems designers and OEMs have increasingly required solutions to the technical challenges described above in order to take advantage of continuing speed and performance enhancements in microprocessor and memory ICs. These customers have also continued to migrate from single-part vendors to suppliers who can provide multiple parts for their systems, both to reduce the number of vendors they must deal with and to address interoperability requirements among the interface ICs within the system. Due to the short design times and product life cycles these customers face for their own products, they are requiring rapid response time and part availability from interface IC vendors. Interface IC vendors are further required to accomplish these tasks in a cost-effective manner that flexibly responds to specific customer needs.

## THE PERICOM STRATEGY

We are a leading supplier of high-performance interface ICs. While our products address a wide spectrum of applications, we have focused our resources on solving bandwidth and interoperability bottlenecks in customers' systems using our high performance interface technology. With processing power doubling every 18 months historically, the speed at which microprocessors can access memory becomes the bandwidth constraint in high speed computing. We have developed solutions with Intel, IBM and other market leaders to support higher-speed processor-memory interfacing, which support the latest memory standards, on both the system motherboard and the memory modules. In server applications, we support higher system bandwidth through the use of high frequency clock products, which provide timing signals to synchronize data transfer, and bus switches that isolate a system's memory modules from the bus when they are not being addressed.

Products are defined in collaboration with the industry-leading OEMs and industry enablers such as Intel and IBM, and our modular design methodology shortens our time to market and time to volume relative to our competitors. The key elements of our strategy are:

**Market Focus.** Our market strategy is to focus on the high-growth, high-performance segments of the computing, networking and telecommunications markets. Currently, we design and sell products for specific high-volume applications within these target markets, including notebook computers, computer servers, local area network (LAN), and wide area network (WAN), switches, routers and hubs and multi-function office peripherals such as printers and copiers. Our customers and end-users of our products include a number of leading OEMs in each market:

- Notebook market: Acer, Apple, HP/Compaq, Dell, Fujitsu, IBM and Toshiba
- Telecommunications equipment market: Huawei, Lucent, Motorola, Samsung and ZTE
- PC Server market: HP/Compaq, Dell, Intel, IBM and Sun
- Network equipment market: Cisco, HP, Lucent and Nortel
- Printer and copier market: Canon, HP, Lexmark, Sharp and Xerox

We intend to pursue new opportunities in markets where our rapid-cycle IC design and development expertise and understanding of the product evolution of our customers gives us the opportunity to become a leading solution supplier.

**Customer Focus.** Our customer strategy is to use a superior level of responsiveness to customer needs to continually expand our customer base as well as sell a wider range of products to our existing and targeted new customers. Key elements of our customer strategy are:

- Penetrate target accounts through joint product development. We approach prospective customers primarily by working with their system design engineers at the product specification stage with the goal that one or more Pericom ICs will be incorporated into a new system design. Our understanding of our customers' requirements combined with our ability to develop and deliver reliable, high-performance products within our customers' product introduction schedules has enabled us to establish strong relationships with many leading OEMs.
- Solidify customer relationships through superior responsiveness. We believe that our customer service orientation is a significant competitive advantage. We seek to maintain short product lead times and provide our customers with excellent on-schedule delivery, in part by having available adequate finished goods inventory for anticipated customer demands. We emphasize product quality for our products and have been ISO-9001 certified since March 1995.
- Expand customer relationships through broad-based solutions. We aim to grow our business with existing customers by offering product lines that provide increasingly extensive solutions for our customers' high-speed interfacing needs. By providing our customers with superior support in existing programs and anticipating our customers' needs in next-generation products, we have often been able to substantially increase our overall volume of business with those customers. With larger customers we have also initiated

- electronic data interchange, or EDI, and remote warehousing programs, annual purchase and supply programs, joint development projects and other services intended to enhance our position as a key vendor.
- Responding to our customer requirements is one of the highest priorities of Pericom. In order to accomplish this, we have implemented an automated quoting system from Azerity, a leader in e-business solutions. Pericom can respond very quickly to our customer needs and offer them superior service. With the implementation of Azerity's ProChannel, Pericom has also been able to streamline a number of internal procedures.

**Technology Focus.** Our technology strategy is to maintain a leading position in the development of new, higher-performance interface ICs by continuing to design additional core cells that address the more challenging problems of signal interface as electronic systems become faster and require lower power and voltages. Our primary efforts are in the creation of additional proprietary digital, analog and mixed-signal functionality. We are working closely with our wafer suppliers to incorporate their advanced CMOS process technologies to improve our ability to introduce next generation products expeditiously. We are continuing to expand our patent portfolio with the goal of providing increasingly proprietary product lines.

**Manufacturing Focus.** Our manufacturing strategy is closely integrated with our focus on customer needs. Central to this strategy is our intent to support high-volume shipment requirements on short notice from customers. We design products so that we may manufacture many different ICs from a single partially-processed wafer. Accordingly, we keep inventory in the form of wafer banks, from which wafers can be completed to produce a variety of specific ICs in as little as two to four weeks. This approach has enabled us to reduce our overall work-in-process inventory while providing increased availability to produce a variety of finished products. In addition, we keep some inventory in the form of die banks, which can become finished product in two weeks or less. We have established relationships with three leading foundries, Chartered Semiconductor Manufacturing Pte, Ltd. ("Chartered"), Taiwan Semiconductor Manufacturing Corporation ("TSMC"), and Hynix Semiconductor America, Inc ("Hynix"). We are maintaining our relationship with New Japan Radio Corporation ("NJRC") for some of our products that are manufactured in high-voltage CMOS processes.

**Strategic and Collaborative Relationships Focus.** We pursue a strategy of entering into new relationships and expanding existing relationships with companies that engage in the product design, manufacturing and marketing of ICs. We believe that these relationships have enabled us to access additional design and application expertise, accelerate product introductions, reduce costs and obtain additional needed capacity. In product design, we have engaged Pericom Technology, Inc., an affiliated company, and other design houses to develop interface ICs as a means of rapidly expanding our product portfolio. We have established collaborative relationships with leading wafer manufacturers that have high performance digital core libraries that we can use in our future products. We have also made strategic investments in several companies with whom we have obtained rights to certain technologies and are engaged in collaborative research and development.

## PRODUCTS

We have used our expertise in high-performance digital, analog and mixed-signal IC design, our reusable core cell library and our modular design methodology to achieve a rapid rate of new product introductions. Within each of our four product lines, the product portfolio has evolved from a standard building block into both standard products of increasing performance and application-specific standard products, or ASSPs, which are tailored to meet a specific high volume application. Within each product family we continue to address the common trends of decreasing supply voltage, higher integration and faster speeds.

### **SiliconSwitch™**

Through our SiliconSwitch™ product line, we offer a broad range of high-performance ICs for switching digital and analog signals. The ability to switch or route high-speed digital or analog signals with minimal delay and signal distortion is a critical requirement in many high-speed computers, networking and multimedia applications. Historically, systems designers have used mechanical relays and solid-state relays that have

significant disadvantages as compared to IC switches: mechanical relays are bulky, dissipate significant power and have very low response times; solid-state relays are expensive.

**Digital Switches.** We offer a family of digital switches in 8-, 16- and 32-bit densities that address the switching needs of high-performance systems. These digital switches offer performance and cost advantages over traditional switch functions, offering both low on-resistance and capacitance, low propagation delay (less than 250 picoseconds), low standby current (less than 1 microamp) and series resistor options that support low electromagnetic interference, or EMI, emission requirements. Applications for our digital switches include 5-volt to 3.3-volt signal translation, high-speed data transfer and switching between microprocessors, PCI slots, and multiple memories, and hot plug interfaces in notebook and desktop computers, servers and switching hubs and routers. We also have two families of 3.3-volt switches offering industry-leading performance in switching times, and low capacitance for bus isolation applications. We continued to expand the number of devices and the performance of these families during fiscal 2003.

**Analog Switches.** We offer a family of analog switches for low-voltage (2- to 7-volt) applications such as multimedia audio and video signal switching with enhanced characteristics such as low power, high bandwidth, low crosstalk and low distortion to maintain analog signal integrity. Our analog switches have significantly lower distortion than traditional analog switches due to our advanced CMOS switch design. To support space-constrained applications, such as wireless handsets and global positioning system receivers, we introduced a new set of 5-volt Low R-on 1 Ohm switches in tiny SOT-23, So-7 and new 6-pin and 12-pin ultra small TDFN packages. To complement this low-voltage family we also offer a higher voltage (17-volt) analog switch family for applications requiring higher signal range, such as instrumentation, telecommunications and industrial control.

**LAN Switches and Video Switches.** We offer a line of application specific standard products ("ASSP") switches that address the high performance demands of 10/100/1000 Ethernet LANs and high bandwidth video switches that enable the switching between different video sources associated with video graphic cards and flat panel displays. We continue to expand our innovations in this area to address next generation Networking and Media platform.

### **SiliconInterface™**

Through our SiliconInterface™ product line, we offer a broad range of high- performance 5-volt, 3.3-volt, 2.5-volt, and 1.8-volt CMOS logic interface circuits. These products provide logic functions to handle data transfer between microprocessors and memory, bus exchange, backplane interface and other logic interface functions where high-speed, low-power, low-noise and high-output drive characteristics are essential.

**5-Volt Interface Logic.** Our high-speed 5-volt interface logic products in 8- and 16-bit configurations address specific system applications, including a "Quiet Series" family for high-speed, low-noise, point-to-point data transfer in computing and networking systems and a "Balanced Drive" family with series resistors at output drivers to reduce switching noise in high-performance computers. We continue to provide a complete portfolio of 5-volt FCT logic products that supports many legacy data communications and telecommunications switch platforms.

**3.3-Volt Interface Logic.** To support the trend toward lower system voltages for higher silicon integration, from 8-Bit to 32-Bits, system performance and power savings we offer five families of 3.3-volt interface logic to address a range of performance and cost requirements with very low power consumption. As the industry continues to migrate to 3.3-volt interface, we continued to enhance and expand our 3.3-volt logic product portfolio with the introduction of a new 16-bit logic family called LVTC targeted to replace power hungry LVT BiCMOS logic in the Telecom market place. Our ASIC design methodology and existing cell based designs contribute to our rapid product development in this area.

**2.5-Volt Interface Logic.** Our 2.5-volt product families include three new logic families to address 2.5- and 1.8-volt operation. We have a family of products with high output current offering sub-2.5 nanosecond propagation delays and the lowest power consumption in its class. We also have a lower balanced drive family with a

propagation delay of less than 2 nanoseconds to support high-speed processor-memory interfacing and a balanced drive family optimized for low-noise operation at very low voltages. Increasingly, networking, PC and memory module manufacturers are demanding application specific logic products. We have met this challenge in 2003 by introducing new logic functions that support next generation DDR I-333 and DDR I-400 register products that meet the demands of current and next generation DDR I memory module applications associated with the PC & Server markets.

***SOTiny Gates.*** To address the growing needs of the marketplace requirements of sourcing single and dual gate logic solutions, we expanded our SOTiny gate families to include 5V TTL translators. These products operate from 1.65v to 5v to address the interface needs associated in many applications. We continue to tap into new product markets in the areas of communication, PC peripherals, and consumer digital systems.

### ***SiliconClock™***

In high bandwidth systems data transfer needs to be synchronized, creating a demand for timing products. Our timing products provide the precise timing signals needed to ensure reliable data transfer at high speeds in applications ranging from notebook computers to network switches. As systems continue to grow in processing power and complexity, the demand for these products is expected to accelerate. The demand for precision will also increase as timing margins shrink in higher bandwidth systems.

Through our SiliconClock™ product line, we offer a broad range of general-purpose solutions including voltage control crystal oscillators (VCXO), clock buffers, zero-delay clock drivers and frequency synthesizer products for a wide range of microprocessor systems, as well as a number of ASSP timing products for laser printers, registered memory modules, storage area networks, servers, networking, and set-top box applications.

***Clock Buffers and Zero-Delay Clock Drivers.*** Clock buffers receive a clock signal from a frequency source and create multiple copies of the same frequency for distribution across system boards. We offer 2.5-volt, 3.3-volt and 5-volt clock buffers for high-speed, low-skew applications in computers and networking equipment. For systems that require higher performance, we are developing differential clock buffers with frequencies up to 800MHz. Zero-delay clocks virtually eliminate propagation delays by synchronizing the clock outputs with the incoming frequency source. Our 5-volt, 3.3-volt, 2.5-volt and 1.8-volt zero-delay clock drivers offer frequencies of up to 270 MHz for applications in networking switches, routers and hubs, computer servers, and memory modules. Timing products to support the emerging double data rate (DDR) and DDR II memory technologies are available today.

***Clock Frequency Synthesizers.*** Clock frequency synthesizers generate various output frequencies using a single input frequency source. Clock frequency synthesizers are used to provide critical timing signals to microprocessors, memory and peripheral functions. Our clock synthesizers support a wide range of microprocessor systems and their associated integrated chipsets for server, notebook and desktop PC products. We also offer a clock synthesizer developed specifically for laser printer products.

***Programmable Clocks.*** In large computing and communications systems customers need to provide precise timing across large printed circuit boards, or PCBs. At the very high frequencies used today these large PCB traces can result in significant timing delays and matching these delays (or timing skew) can be a significant challenge for the system designer. We have responded to this challenge with a family of programmable skew clock products.

## **SiliconConnect™**

SiliconConnect™ is our newest product line and offers the highest level of complexity and integration among our products. It consists of our LVDS family of high-speed differential drivers, receivers, and transceivers, PCI Bridges, and our first SERDES product.

To support higher system bandwidth at acceptable noise and power levels customers are increasingly moving to serial rather than parallel architectures and using differential signaling to reduce noise and EMI. We continue to refine our LVDS product families by offering a comprehensive portfolio of drivers, receivers and transceivers with data rates of 660 megabits per second, or Mbps, and allowing point-to-point connections over distances up to 10 meters. This new low-voltage differential signaling, or LVDS, standard offers a number of improvements over the older emitter-coupled logic, or ECL, and pseudo emitter-coupled logic, or PECL, in applications requiring lower power consumption and noise. Pericom's LVDS family is approximately 50 products today.

In 2003 we expanded our PCI Bridge product portfolio by introducing 3 Intel compatible devices to the market. These devices complement our current proprietary enhanced 3-port bridge device. Design activity and initial revenues have been promising for these high margin products. We also continued to invest in developing advanced devices including our PCI-X Bridge device, which is in development and, along with customer specific and performance enhanced versions. Bridge devices are used in industrial PCs and certain other computers; on RAID and Fibre Channel cards in the Storage Area Network space; Multi-channel Ethernet NICs; and routers and switches.

We are continuing to enhance and refine our existing product lines, while working to add next-generation products that address new market opportunities on a timely basis. See "Risk Factors; Factors That May Affect Operating Results – If we do not develop products that our customers and end-users design into their products, or if their products do not sell successfully, our business and operating results could be harmed."

## **CUSTOMERS**

The following is a list of some of our customers and end-users:

<b>Computer</b>	<b>Telecommunications</b>	<b>Networking Equipment</b>
Apple	Huawei	Cisco
Asustek	LGE	Lucent
Compal	Motorola	
Dell	Samsung	
Fujitsu	ZTE	
Hitachi		
Hewlett Packard / Compaq		
IBM		
Intel		
Inventec		
Legacy		
Micron		
NEC		
Quanta		
Smart Modular		
Toshiba		
	<b>OEMs &amp; Peripherals</b>	<b>Contract Manufacturing</b>
	Hewlett Packard / Compaq	Celestica
	Lexmark	Foxconn
	Philips / Grundig	Jabil Circuit
	Seagate	Sanmina/SCI
	Xerox	Solectron

Our customers include a broad range of end-user customers and OEMs in the computer, peripherals, networking and telecommunications markets as well as the contract manufacturers that service these markets. Our direct sales are billings directly to a customer who may in turn sell through to an end-user customer. Our end-user customers may buy directly or through our distribution or contract manufacturing channels. In fiscal 2003, our direct sales to Solectron, a global contract manufacturer, and Techmosa, an international distributor, each accounted for approximately 12% of net revenues and direct sales to our top five customers accounted for

approximately 45% of net revenues. In fiscal 2003, one end user customer, Compaq Computer, accounted for 11% of gross revenues less returns and sales to our top five end user customers accounted for 39% of gross revenues less returns. In fiscal 2002, our direct sales to Techmosa accounted for approximately 14% of net revenues and direct sales to our top five customers accounted for approximately 44% of net revenues. In fiscal 2002, three end-user customers each accounted for more than 10% of gross revenues less returns. Dell Computer, Compaq Computer, and Cisco Systems accounted for 15%, 12% and 11% of gross revenues less returns, respectively, and sales to our top five end-user customers were approximately 50% of gross revenues less returns. In fiscal 2001, our direct sales to Techmosa accounted for approximately 10% of net revenues, and direct sales to our top five customers accounted for approximately 36% of net revenues. In fiscal 2001 one end-user customer, Cisco Systems, accounted for approximately 12% of gross revenues less returns and sales to our top five end-user customers were approximately 39% of gross revenues less returns. See " Risk Factors; Factors That May Affect Operating Results – The demand for our products depends on the growth of our end-user customer markets" and "Risk Factors; Factors That May Affect Operating Results – A large portion of our revenues is derived from sales to a few customers, who may cease purchasing from us at any time."

Contract manufacturers have become important customers for us as systems designers in our target markets are increasingly outsourcing portions of their manufacturing. In addition, these contract manufacturers are playing an increasingly vital role in determining which vendors' ICs are incorporated into new designs.

## **DESIGN AND PROCESS TECHNOLOGY**

Our design efforts focus on the development of high-performance digital, analog and mixed-signal ICs. To minimize design cycle times of high-performance products, we use a modular design methodology that has enabled us to produce many new products each year and to meet our customers' need for fast time-to-market response. This methodology uses state-of-the-art computer-aided design software tools such as high-level description language, or HDL, logic synthesis, full-chip mixed-signal simulation, and automated design layout and verification and uses our library of high-performance digital and analog core cells. This family of core cells has been developed over several years and contains high-performance, specialized digital and analog functions not available in commercial ASIC libraries. Among these cells are our proprietary mixed-voltage input/output, or I/O, cells, high-speed, low-noise I/O cells, analog and digital phase-locked loops, or PLLs, charge pumps and data communication transceiver circuits using low voltage differential signaling. We have been granted 61 U.S. patents and have at least 26 U.S. patent applications pending. Another advantage of this modular design methodology is that it allows the application of final design options late in the wafer manufacturing process to determine a product's specific function. This option gives us the ability to use pre-staged wafers, which significantly reduces the design and manufacturing cycle time and enables us to respond rapidly to a customer's prototype needs and volume requirements.

We use advanced CMOS processes to achieve higher performance and lower die cost. Our process and device engineers work closely with our independent wafer foundry partners to develop and evaluate new process technologies. Our process engineers also work closely with circuit design engineers to improve the performance and reliability of our cell library. We currently manufacture a majority of our products using 0.8-, 0.6-, 0.5-, 0.35- and 0.25-micron CMOS process technologies and are currently developing new products using 0.18-micron technology. We are also using a high-voltage CMOS process developed by one of our wafer suppliers in the design of higher voltage switch products.

## **SALES AND MARKETING**

We market and distribute our products through a worldwide network of independent sales representatives and distributors supported by our internal and field sales organization. Sales to domestic and international distributors represented 59% of our net revenues in fiscal 2003, 54% of our net revenues in fiscal 2002 and 54% of our net revenues in fiscal 2001. The percentage increase from fiscal 2002 to fiscal 2003 was largely due to the transition of business to the international distribution channel. Our major distributors in the United States include All American Semiconductor, Arrow Electronics, Inc. Bell Microproducts, Future Electronics and Nu Horizons Electronics. Our major international distributors include Chinatronics (Hong Kong), Desner

Electronics (Singapore), EPCO Technology Co., Ltd. (Taiwan), Internix (Japan), MCM (Japan) and Techmosa (Taiwan).

We have five regional sales offices in the United States and international sales offices in Taiwan, Korea, Singapore, Japan and the United Kingdom. International sales comprised approximately 68% of our net revenues in fiscal 2003, approximately 67% of our net revenues in fiscal 2002, and approximately 49% of our net revenues in fiscal 2001. We also support field sales design-in and training activities with application engineers. Most marketing and all product management personnel are located at our corporate headquarters in San Jose, California. We also have marketing personnel located in Taiwan.

We focus our marketing efforts on market knowledge, product definition, new product introduction, product marketing and advertising. We use advertising both domestically and internationally to market our products independently and in cooperation with our distributors. Pericom product information is available on our web site, which contains overview presentations, technical information on our products, and offers design modeling/applications support plus sample-request capabilities online. We also publish and circulate technical briefs relating to our products and their applications.

We believe that contract manufacturing customers are strategically important and we employ sales and marketing personnel who focus on servicing these customers and on expanding our product sales via these customers to OEMs. In addition, we use programs such as EDI, bonded inventories and remote warehousing to enhance our service and attractiveness to contract manufacturers.

## **MANUFACTURING**

We have adopted a fabless manufacturing strategy by subcontracting our wafer production to independent wafer foundries. We have established collaborative relationships with selected independent foundries with whom we can develop a strategic relationship to the benefit of both parties. We believe that our fabless strategy enables us to introduce high performance products quickly at competitive cost. To date, our principal manufacturing relationships have been with Chartered Semiconductor Manufacturing Pte, Ltd., Taiwan Semiconductor Manufacturing Corporation and Hynix Semiconductor America, Inc. We have also used New Japan Radio Corporation and Austria Mikro Systeme GmbH as foundries. We have an ongoing effort to qualify new foundry vendors that offer cost or other advantages.

We primarily rely on foreign subcontractors for the assembly of our products and, to a lesser extent, for the testing and packaging of our finished products. Some of these subcontractors are our single source supplier for certain new packages. We perform some testing and packaging of our finished products in our in-house manufacturing facility.

## **COMPETITION**

The semiconductor industry is intensely competitive. Significant competitive factors in the market for high-performance ICs include the following:

- product features and performance;
- price;
- product quality;
- success in developing new products;
- timing of new product introductions;
- general market and economic conditions;
- adequate wafer fabrication capacity and sources of raw materials;
- efficiency of production; and
- ability to protect intellectual property rights and proprietary information.

Our competitors include Cypress Semiconductor Corporation, Fairchild Semiconductor Corporation, Integrated Circuit Systems, Inc., Integrated Device Technology, Inc., Intel Corp. Maxim Integrated Products, Inc., Philips and Texas Instruments, Inc. Most of those competitors have substantially greater financial, technical, marketing, distribution and other resources, broader product lines and longer-standing customer relationships than we do. We also compete with other major or emerging companies that sell products to certain segments of our markets. Competitors with greater financial resources or broader product lines may have a greater ability to sustain price reductions in our primary markets in order to gain or maintain market share. We also face competition from the makers of ASICs and other system devices. These devices may include interface logic functions, which may eliminate the need or sharply reduce the demand for our products in particular applications.

## **RESEARCH AND DEVELOPMENT**

We believe that the continued timely development of new interface ICs is essential to maintaining our competitive position. Accordingly, we have assembled a team of highly skilled engineers whose activities are focused on the development of signal transfer, routing and timing technologies and products. We have design centers located in San Jose, California, Hong Kong, and Taiwan. Research and development expenses were \$11.3 million in fiscal 2003, \$11.7 million in fiscal 2002 and \$11.0 million in fiscal 2001. Additionally, we actively seek cooperative product development relationships.

## **INTELLECTUAL PROPERTY**

In the United States, we hold 61 patents covering certain aspects of our product designs and have at least 26 additional patent applications pending. We expect to continue to file patent applications where appropriate to protect our proprietary technologies; however, we believe that our continued success depends primarily on factors such as the technological skills and innovation of our personnel, rather than on our patents.

## **EMPLOYEES**

As of June 30, 2003, we had 215 full-time employees (4 are temporary employees) and 1 part-time employee, including 39 in sales, marketing and customer support, 71 in manufacturing, assembly and testing, 81 in engineering and quality assurance and 25 in finance and administration, including information systems. We have never had a work stoppage and no employee is represented by a labor organization. We consider our employee relations to be good.

## **AVAILABLE INFORMATION**

We file electronically with the Securities and Exchange Commission our annual reports on Form 10-K, quarterly reports on Form 10-Q, current reports on Form 8-K, and amendments to those reports pursuant to Section 13(a) or 15(d) of the Securities Exchange Act of 1934. The Securities and Exchange Commission maintains an Internet site at <http://www.sec.gov> that contains these reports, proxy and information statements. We make available on our website at <http://www.pericom.com>, free of charge, copies of these reports as soon as reasonably practicable after filing or furnishing the information to the SEC.

## **RISK FACTORS; FACTORS THAT MAY AFFECT OPERATING RESULTS**

In addition to other information contained in this Form 10-K, investors should carefully consider the following factors that could adversely affect our business, financial condition and operating results as well as adversely affect the value of an investment in our common stock. This Form 10-K includes "forward-looking statements" within the meaning of Section 27A of the Securities Act of 1933, as amended, and Section 21E of the Securities Exchange Act of 1934, as amended. All statements other than statements of historical fact are "forward-looking statements" for purposes of these provisions, including any statements regarding projections of earnings, revenues or other financial items; plans and objectives of management for future operations; future expenditures; proposed new products or services and their development schedule; industry, technological or market trends; our ability to address the need for application specific logic products; our ability to respond rapidly to customer needs; expanding product sales; future economic conditions or performance, and any statement of assumptions

underlying any of the foregoing. In some cases, forward-looking statements can be identified by the use of terminology such as "may," "will," "expects," "plans," "anticipates," "estimates," "potential," or "continue," or the negative thereof or other comparable terminology. Although we believe that the expectations reflected in the forward-looking statements contained herein are reasonable, there can be no assurance that such expectations or any of the forward-looking statements will prove to be correct, and actual results could differ materially from those projected or assumed in the forward-looking statements. Some of the factors that could cause our actual results to differ materially are set forth below and elsewhere in this report. All forward-looking statements and reasons why results may differ included in this Form 10-K are made as of the date hereof, and we assume no obligation to update any such forward-looking statement or reason why actual results may differ.

**Our results of operations have been adversely affected by the ongoing slowdown in the global economy.**

Over the past several years, the global economy has been experiencing a slowdown due to many factors, including decreased consumer confidence, unemployment, the ongoing threat and cost of terrorism and reduced corporate profits and capital spending. As a result of these unfavorable economic conditions, we continue to experience reduced new customer order rates. If these economic conditions in the United States continue or worsen or if a wider or global economic slowdown occurs, our business, financial condition and results of operations may be materially and adversely affected.

**Downturns in the semiconductor industry, rapidly changing technology and evolving industry standards can harm our operating results.**

The semiconductor industry has historically been cyclical and periodically subject to significant economic downturns--characterized by diminished product demand, accelerated erosion of selling prices and overcapacity--as well as rapidly changing technology and evolving industry standards. The semiconductor industry continued to be in such a downturn period throughout 2003. Our operating results have been harmed by excess supply in the semiconductor industry. Accordingly, we may in the future experience substantial period-to-period fluctuations in our business and operating results due to general semiconductor industry conditions, overall economic conditions or other factors. Our business is also subject to the risks associated with the effects of legislation and regulations relating to the import or export of semiconductor products.

**Intense competition in the semiconductor industry may reduce the demand for our products or the prices of our products, which could reduce our revenues.**

The semiconductor industry is intensely competitive. Our competitors include Cypress Semiconductor Corporation, Fairchild Semiconductor Corporation, Integrated Circuit Systems, Inc., Integrated Device Technology, Inc., Intel Corp., Maxim Integrated Products, Inc., Philips and Texas Instruments, Inc. Most of those competitors have substantially greater financial, technical, marketing, distribution and other resources, broader product lines and longer-standing customer relationships than we do. We also compete with other major or emerging companies that sell products to certain segments of our markets. Competitors with greater financial resources or broader product lines may have a greater ability to sustain price reductions in our primary markets in order to gain or maintain market share.

We believe that our future success will depend on our ability to continue to improve and develop our products and processes. Unlike us, many of our competitors maintain internal manufacturing capacity for the fabrication and assembly of semiconductor products. This ability may provide them with more reliable manufacturing capability, shorter development and manufacturing cycles and time-to-market advantages. In addition, competitors with their own wafer fabrication facilities that are capable of producing products with the same design geometries as ours may be able to manufacture and sell competitive products at lower prices. Any introduction of products by our competitors that are manufactured with improved process technology could seriously harm our business. As is typical in the semiconductor industry, our competitors have developed and marketed products that function similarly or identically to ours. If our products do not achieve performance, price, size or other advantages over products offered by our competitors, our products may lose market share. Competitive pressures could also reduce market acceptance of our products, reduce our prices and increase our expenses.

We also face competition from the makers of ASICs and other system devices. These devices may include interface logic functions, which may eliminate the need or sharply reduce the demand for our products in particular applications.

**Product price declines and fluctuations may cause our future financial results to vary.**

Historically, selling prices in the semiconductor industry generally, as well as for our products, have decreased significantly over the life of each product. We expect that selling prices for our existing products will continue to decline over time and that average selling prices for our new products will decline significantly over the lives of these products. Declines in selling prices for our products, if not offset by reductions in the costs of producing these products or by sales of new products with higher gross margins, would reduce our overall gross margins and could seriously harm our business.

**If we do not develop products that our customers and end-users design into their products, or if their products do not sell successfully, our business and operating results would be harmed.**

We have relied in the past and continue to rely upon our relationships with our customers and end-users for insights into product development strategies for emerging system requirements. We generally incorporate new products into a customer's or end-user's product or system at the design stage. However, these design efforts, which can often require significant expenditures by us, may precede product sales, if any, by a year or more. Moreover, the value to us of any design win will depend in large part on the ultimate success of the customer's or end-user's product and on the extent to which the system's design accommodates components manufactured by our competitors. If we fail to achieve design wins or if the design wins fail to result in significant future revenues, our operating results would be harmed. If we have problems developing or maintaining our relationships with our customers and end-users, our ability to develop well-accepted new products may be impaired.

**The trading price of our common stock and our operating results are likely to fluctuate substantially in the future.**

The trading price of our common stock has been and is likely to continue to be highly volatile. Our stock price could fluctuate widely in response to factors some of which are not within our control, including:

- changes in earnings estimates by analysts; and price and volume fluctuations in the overall stock market, which have particularly affected the market prices of many high technology companies;
- general conditions in the semiconductor and electronic systems industries;
- quarter-to-quarter variations in operating results; and
- announcements of technological innovations or new products by us or our competitors.

**In the past, our quarterly operating results have varied significantly and are likely to fluctuate in the future. A wide variety of factors affect our operating results. These factors include the following:**

- general conditions in the semiconductor industry;
- changes in our product mix;
- a decline in the gross margins of our products due to price declines or cost increases;
- expenses incurred in obtaining, enforcing, and defending intellectual property rights;
- the timing of new product introductions and announcements by us and by our competitors;
- customer acceptance of new products introduced by us;
- delay or decline in orders received from distributors;
- growth or reduction in the size of the market for interface ICs;
- the availability of manufacturing capacity with our wafer suppliers;
- changes in manufacturing costs;
- fluctuations in manufacturing yields;
- the ability of customers to pay us; and

- increased research and development expenses associated with new product introductions or process changes.

All of these factors are difficult to forecast and could seriously harm our operating results. Our expense levels are based in part on our expectations regarding future sales and are largely fixed in the short term. Therefore, we may be unable to reduce our expenses fast enough to compensate for any unexpected shortfall in sales. Any significant decline in demand relative to our expectations or any material delay of customer orders could harm our operating results. In addition, if our operating results in future quarters fall below public market analysts' and investors' expectations, the market price of our common stock would likely decrease.

**The markets for our products are characterized by rapidly changing technology, and our financial results could be harmed if we do not successfully develop and implement new manufacturing technologies or develop, introduce and sell new products.**

The markets for our products are characterized by rapidly changing technology, frequent new product introductions and declining selling prices over product life cycles. We currently offer over 700 products. Our future success depends upon the timely completion and introduction of new products, across all of our product lines, at competitive price and performance levels. The success of new products depends on a variety of factors, including the following:

- product performance and functionality;
- customer acceptance;
- competitive pricing;
- successful and timely completion of product development;
- sufficient wafer fabrication capacity; and
- achievement of acceptable manufacturing yields by our wafer suppliers.

We may also experience delays, difficulty in procuring adequate fabrication capacity for the development and manufacture of new products or other difficulties in achieving volume production of these products. Even relatively minor errors may significantly affect the development and manufacture of new products. If we fail to complete and introduce new products in a timely manner at competitive price and performance levels, our business would be significantly harmed.

**The demand for our products depends on the growth of our end users' markets.**

Our continued success depends in large part on the continued growth of markets for the products into which our semiconductor products are incorporated. These markets include the following:

- computers and computer related peripherals;
- data communications and telecommunications equipment;
- electronic commerce and the Internet; and
- consumer electronics equipment.

Any decline in the demand for products in these markets could seriously harm our business, financial condition and operating results. These markets have also historically experienced significant fluctuations in demand. We may also be seriously harmed by slower growth in the other markets in which we sell our products.

**Our contracts with our wafer suppliers do not obligate them to a minimum supply or set prices. Any inability or unwillingness of our wafer suppliers generally, and Chartered Semiconductor Manufacturing Ltd. in particular, to meet our manufacturing requirements would delay our production and product shipments and harm our business.**

In fiscal 2003, 2002 and 2001 we purchased approximately 72%, 68% and 59%, respectively, of our wafers from Chartered Semiconductor Manufacturing Ltd. In fiscal 2003, only three other suppliers manufactured the remainder of our wafers. In fiscal 2002 and 2001, five other suppliers manufactured the remainder of our wafers. Our reliance on independent wafer suppliers to fabricate our wafers at their production facilities subjects us to possible risks such as:

- lack of adequate capacity;
- lack of available manufactured products;
- quality or other technical difficulties;
- lack of control over delivery schedules; and
- unanticipated changes in wafer prices.

Any inability or unwillingness of our wafer suppliers generally, and Chartered in particular, to provide adequate quantities of finished wafers to meet our needs in a timely manner would delay our production and product shipments and seriously harm our business.

At present, we purchase wafers from our suppliers through the issuance of purchase orders based on our rolling six-month forecasts. The purchase orders are subject to acceptance by each wafer supplier. We do not have long-term supply contracts which obligate our suppliers to a minimum supply or set prices. We also depend upon our wafer suppliers to participate in process improvement efforts, such as the transition to finer geometries. If our suppliers are unable or unwilling to do so, our development and introduction of new products could be delayed. Furthermore, sudden shortages of raw materials or production capacity constraints can lead wafer suppliers to allocate available capacity to customers other than us or for the suppliers' internal uses, interrupting our ability to meet our product delivery obligations. Any significant interruption in our wafer supply would seriously harm our operating results and our customer relations. Our reliance on independent wafer suppliers may also lengthen the development cycle for our products, providing time-to-market advantages to our competitors that have in-house fabrication capacity.

In the event that our suppliers are unable or unwilling to manufacture our key products in required volumes, we will have to identify and qualify additional wafer foundries. The qualification process can take up to six months or longer. Furthermore, we are unable to predict whether additional wafer foundries will become available to us or will be in a position to satisfy any of our requirements on a timely basis.

**Our pending acquisition of Saronix and other potential future acquisitions may not be successful because we have not made acquisitions in the past.**

We have entered into a definitive purchase option agreement to acquire certain assets and certain liabilities of Saronix LLC, a private frequency timing products company, subject to the completion of due diligence and customary closing conditions. The purchase price is expected to be under \$10 million, much of which has been advanced to Saronix in the form of convertible secured loans. On September 24, 2003, we exercised this option to acquire certain assets and certain liabilities of Saronix LLC. We anticipate that this acquisition will be completed on October 1, 2003, but there can be no assurance that it will be completed on that date.

The Saronix acquisition will be our first acquisition. In the past, we have depended on internal growth and not made any acquisitions.

The Saronix acquisition and other potential future acquisitions could result in the following:

- large one-time write-offs;
- diversion of management's attention from other business concerns;
- risks of entering geographic and business markets in which we have no or limited prior experience and potential loss of key employees of acquired organizations;

- potentially dilutive issuances of equity securities;
- the incurrence of debt and contingent liabilities or amortization expenses related to goodwill and other intangible assets; and
- difficulties in the assimilation of operations, personnel, technologies, products and the information systems of the acquired companies.

We are not certain that we will be able to successfully integrate the Saronix business or any other businesses, products, technologies or personnel that may be acquired in the future. Our failure to do so could seriously harm our business.

As part of our business strategy, we expect to seek other acquisition prospects, in addition to Saronix, that would complement our existing product offerings, improve market coverage or enhance our technological capabilities. Although we are evaluating acquisition and strategic investment opportunities on an ongoing basis, we may not be able to locate suitable acquisition or investment opportunities. In addition, from time to time, we invest in other companies, without actually acquiring them, and such investments involve many of the same risks as are involved with acquisitions. For example, we previously made a bridge loan investment in a start up, high technology company, and we subsequently had to write off this loan in light of the financial condition of that company.

**We depend on single or limited source assembly subcontractors with whom we do not have written contracts. Any inability or unwillingness of our assembly subcontractors to meet our assembly requirements would delay our product shipments and harm our business.**

We primarily rely on foreign subcontractors for the assembly and packaging of our products and, to a lesser extent, for the testing of finished products. Some of these subcontractors are our single source supplier for some of our new packages. In addition, changes in our or a subcontractor's business could cause us to become materially dependent on a single subcontractor. We have from time to time experienced difficulties in the timeliness and quality of product deliveries from our subcontractors and may experience similar or more severe difficulties in the future. We generally purchase these single or limited source components or services pursuant to purchase orders and have no guaranteed arrangements with these subcontractors. These subcontractors could cease to meet our requirements for components or services, or there could be a significant disruption in supplies from them, or degradation in the quality of components or services supplied by them. Any circumstance that would require us to qualify alternative supply sources could delay shipments, result in the loss of customers and limit or reduce our revenues.

**We may have difficulty accurately predicting revenues for future periods.**

Our expense levels are based in part on anticipated future revenue levels, which can be difficult to predict. Our business is characterized by short-term orders and shipment schedules. We do not have long-term purchase agreements with any of our customers, and customers can typically cancel or reschedule their orders without significant penalty. We typically plan production and inventory levels based on forecasts of customer demand generated with input from customers and sales representatives. Customer demand is highly unpredictable and can fluctuate substantially. If customer demand falls significantly below anticipated levels, our profit would be reduced.

**We compete with others to attract and retain key personnel, and any loss of, or inability to attract, key personnel would harm us.**

To a greater degree than non-technology companies, our future success will depend on the continued contributions of our executive officers and other key management and technical personnel. None of these individuals has an employment agreement with us and each one would be difficult to replace. We do not maintain any key person life insurance policies on any of these individuals. The loss of the services of one or more of our executive officers or key personnel or the inability to continue to attract qualified personnel could delay product development cycles or otherwise harm our business, financial condition and results of operations.

Our future success also will depend on our ability to attract and retain qualified technical, marketing and management personnel, particularly highly skilled design, process and test engineers, for whom competition is intense. In particular, the availability of qualified engineers is limited and competition among companies for skilled and experienced engineering personnel is very strong. During strong business cycles, we expect to experience difficulty in filling our needs for qualified engineers and other personnel.

**Our limited ability to protect our intellectual property and proprietary rights could harm our competitive position.**

Our success depends in part on our ability to obtain patents and licenses and preserve other intellectual property rights covering our products and development and testing tools. In the United States, we hold 61 patents covering certain aspects of our product designs and have at least 26 additional patent applications pending. Copyrights, mask work protection, trade secrets and confidential technological know-how are also key to our business. Additional patents may not be issued to us or our patents or other intellectual property may not provide meaningful protection. We may be subject to, or initiate, interference proceedings in the U.S. Patent and Trademark Office. These proceedings can consume significant financial and management resources. We may become involved in litigation relating to alleged infringement by us of others' patents or other intellectual property rights. This type of litigation is frequently expensive to both the winning party and the losing party and takes up significant amounts of management's time and attention. In addition, if we lose such a lawsuit, a court could require us to pay substantial damages and/or royalties or prohibit us from using essential technologies. For these and other reasons, this type of litigation could seriously harm our business. Also, although we may seek to obtain a license under a third party's intellectual property rights in order to bring an end to certain claims or actions asserted against us, we may not be able to obtain such a license on reasonable terms or at all. See Item 3, "Legal Proceedings".

Because it is important to our success that we are able to prevent competitors from copying our innovations, we intend to continue to seek patent, trade secret and mask work protection for our technologies. The process of seeking patent protection can be long and expensive, and we cannot be certain that any currently pending or future applications will actually result in issued patents, or that, even if patents are issued, they will be of sufficient scope or strength to provide meaningful protection or any commercial advantage to us. Furthermore, others may develop technologies that are similar or superior to our technology or design around the patents we own.

We also rely on trade secret protection for our technology, in part through confidentiality agreements with our employees, consultants and third parties. However, these parties may breach these agreements. In addition, the laws of some territories in which we develop, manufacture or sell our products may not protect our intellectual property rights to the same extent as do the laws of the United States.

The process technology used by our independent foundries, including process technology that we developed with our foundries, can generally be used by them to produce their own products or to manufacture products for other companies including our competitors. In addition, we may not have the right to implement key process technologies used to manufacture some of our products with foundries other than our present foundries.

**We may not provide adequate allowances for exchanges, returns and concessions.**

We recognize revenue from the sale of products when shipped, less an allowance based on future authorized and historical patterns of returns, price protection, exchanges and other concessions. We believe our methodology and approach are appropriate. However, if the actual amounts we incur exceed the allowances, it could decrease our revenue and corresponding gross profit.

**The complexity of our products makes us highly susceptible to manufacturing problems, which could increase our costs and delay our product shipments.**

The manufacture and assembly of our over 700 products are highly complex and sensitive to a wide variety of factors, including:

- the level of contaminants in the manufacturing environment;
- impurities in the materials used; and
- the performance of manufacturing personnel and production equipment.

In a typical semiconductor manufacturing process, silicon wafers produced by a foundry are cut into individual die. These die are assembled into individual packages and tested for performance. Our wafer fabrication suppliers have from time to time experienced lower than anticipated yields of suitable die. In the event of such decreased yields, we would incur additional costs to sort wafers, an increase in average cost per usable die and an increase in the time to market for our products. These conditions could reduce our net revenues and gross margin and harm our customer relations.

We do not manufacture any of our products. Therefore, we are referred to in the semiconductor industry as a "fabless" producer. Consequently, we depend upon third party manufacturers to produce semiconductors that meet our specifications. We currently have third party manufacturers that can produce semiconductors that meet our needs. However, as the industry continues to progress to smaller manufacturing and design geometries, the complexities of producing semiconductors will increase. Decreasing geometries may introduce new problems and delays that may affect product development and deliveries. Due to the nature of the industry and our status as a "fabless" semiconductor company, we could encounter fabrication-related problems that may affect the availability of our products, delay our shipments or increase our costs.

**A large portion of our revenues is derived from sales to a few customers, who may cease purchasing from us at any time.**

A relatively small number of customers have accounted for a significant portion of our net revenues in each of the past several fiscal years. We expect this trend to continue for the foreseeable future. Soletron, a global contract manufacturer and Techmosa, an international distributor that in turn ships to many end users, each accounted for approximately 12% of net revenues during fiscal 2003. Sales to our top five direct customers accounted for approximately 45% of net revenues in fiscal 2003. Of our end-user customers, Compaq Computer accounted for approximately 11% of gross revenues less returns during fiscal 2003. Sales to our top five end-user customers accounted for approximately 39% of gross revenues less returns in fiscal 2003.

We do not have long-term sales agreements with any of our customers. Our customers are not subject to minimum purchase requirements, may reduce or delay orders periodically due to excess inventory and may discontinue selling our products at any time. Our distributors typically offer competing products in addition to ours. In fiscal 2003, sales to domestic and international distributors represented approximately 59% of net revenues. The loss of one or more significant customers, or the decision by a significant distributor to carry the product lines of our competitors, could decrease our revenues.

**Almost all of our wafer suppliers and assembly subcontractors are located in southeast Asia, which exposes us to the problems associated with international operations.**

Almost all of our wafer suppliers and assembly subcontractors are located in southeast Asia, which exposes us to risks associated with international business operations, including the following:

- disruptions or delays in shipments;
- import and export controls;
- changes in economic conditions in the countries where these subcontractors are located;
- currency fluctuations;
- changes in political conditions;
- potentially reduced protection for intellectual property;
- foreign governmental regulations; and

- changes in tax laws, tariffs and freight rates.

In particular, there is a potential risk of conflict and further instability in the relationship between Taiwan and the People's Republic of China. Conflict or instability could disrupt the operations of one of our principal wafer suppliers and several of our assembly subcontractors located in Taiwan.

**Because we sell our products to customers outside of the United States, we face foreign business, political and economic risks that could seriously harm us.**

In fiscal 2003, approximately 60% of our net revenues derived from sales in Asia and approximately 8% from sales in Europe. We expect that export sales will continue to represent a significant portion of net revenues. We intend to expand our sales efforts outside the United States. This expansion will require significant management attention and financial resources and further subject us to international operating risks. These risks include:

- tariffs and other barriers and restrictions;
- unexpected changes in regulatory requirements;
- the burdens of complying with a variety of foreign laws; and
- delays resulting from difficulty in obtaining export licenses for technology.

We are also subject to general geopolitical risks in connection with our international operations, such as political and economic instability and changes in diplomatic and trade relationships. In addition, because our international sales are denominated in U.S. dollars, increases in the value of the U.S. dollar could increase the price in local currencies of our products in foreign markets and make our products relatively more expensive than competitors' products that are denominated in local currencies. Regulatory, geopolitical and other factors could seriously harm our business or require us to modify our current business practices.

**Our shareholder rights plan may adversely affect existing shareholders.**

On March 6, 2002, we adopted a shareholder rights plan that may have the effect of deterring, delaying, or preventing a change in control that otherwise might be in the best interests of our shareholders. Under the rights plan, we issued a dividend of one preferred share purchase right for each share of our common stock held by shareholders of record as of March 21, 2002. Each right entitles shareholders to purchase one one-hundredth of our newly created Series D Junior Participating Preferred Share.

In general, the share purchase rights become exercisable when a person or group acquires 15% or more of our common stock or a tender offer of 15% or more of our common stock is announced or commenced. After such event, our other stockholders may purchase additional shares of our common stock at 50% off of the then-current market price. The rights will cause substantial dilution to a person or group that attempts to acquire us on terms not approved by our Board of Directors. The rights should not interfere with any merger or other business combination approved by our Board of Directors since the rights may be redeemed by us at \$0.001 per right at any time before any person or group acquire 15% or more of our outstanding common stock. These rights expire in March 2012.

**Our operations and financial results could be severely harmed by natural disasters.**

Our headquarters and some of our major suppliers' manufacturing facilities are located near major earthquake faults. One of the foundries we use is located in Taiwan, which has suffered severe earthquakes in the past. We have not experienced significant disruption to our operations due to earthquakes in the past. However, if a major earthquake or other natural disaster were to affect our suppliers, our sources of supply could be interrupted, which would seriously harm our business.

## **ITEM 2. PROPERTIES**

We lease approximately 66,300 square feet of space in San Jose, California in which our headquarters, technology and product development and testing facilities are located. Approximately 53,400 square feet of this facility is leased through July 2004, with renewal options. The remaining 12,900 square feet of this space is leased through May, 2005, with renewal options and is unoccupied. In fiscal 2001 and 2003 there were restructuring charges of \$522,000 and \$1,431,000, respectively, related to this unused leased facility. We also have North American sales offices located in California, Texas, Illinois, and North Carolina as well as International sales offices in Taiwan, Korea, Japan, Singapore and the United Kingdom. We believe our current facilities are adequate to support our needs through the end of fiscal 2004.

## **ITEM 3. LEGAL PROCEEDINGS**

The Company is subject to various routine claims and legal proceedings that arise in the ordinary course of business. The Company is presently not subject to any legal proceedings that could have a material impact on its business or financial condition.

## **ITEM 4. SUBMISSION OF MATTERS TO A VOTE OF SECURITY HOLDERS**

No matters were submitted to a vote of security holders during the fourth quarter of the fiscal year covered by this report.

## PART II

### ITEM 5. MARKET FOR REGISTRANT'S COMMON STOCK AND RELATED STOCKHOLDER MATTERS

The information required by this item regarding equity compensation plans is incorporated by reference to the information set forth in Item 12 of this Annual Report on Form 10-K.

#### COMMON STOCK PRICE RANGE

The Common Stock of the Company began trading publicly on the Nasdaq National Market on October 31, 1997 under the symbol PSEM. Prior to that date, there was no public market for the Common Stock. The Company has not paid cash dividends and has no present plans to do so. It is the policy of the Company to reinvest earnings of the Company to finance expansion of the Company's operations. The following table sets forth for the periods indicated the high and low closing prices of the Common Stock on the Nasdaq National Market. As of June 30, 2003 there were approximately 4,600 holders of record of the Company's Common Stock. During fiscal year 2003, the Company did not sell any unregistered securities.

	<u>High</u>	<u>Low</u>
<b>Fiscal year ended June 30, 2002:</b>		
First Quarter	18.88	12.00
Second Quarter	17.03	12.30
Third Quarter	16.20	10.80
Fourth Quarter	15.27	10.58
<b>Fiscal year ended June 30, 2003:</b>		
First Quarter	11.48	8.14
Second Quarter	10.20	6.30
Third Quarter	9.20	7.33
Fourth Quarter	10.05	7.50

#### SHAREHOLDER RIGHTS PLAN

On March 6, 2002, the Company adopted a shareholder rights plan and, in connection with the plan, the Company has filed a Certificate of Designation designating the rights, preferences and privileges of a new Series D Junior Participating Preferred Share. Pursuant to the plan, the Company issued rights to its stockholders of record as of March 21, 2002, entitling each stockholder to the right to purchase one one-hundredth of a Series D Junior Participating Preferred Share for each share of Common Stock held by the stockholder. Such rights are exercisable only under certain circumstances in connection with a proposed acquisition or merger of the Company.

#### STOCK REPURCHASE PLAN

On October 22, 2001, the Board of Directors authorized the repurchase of up to 2,000,000 shares of the Company's common stock. The Company may repurchase the shares from time to time in open market or private transactions, at the discretion of the Company's management. The Company began repurchasing shares on July 23, 2002. As of September 4, 2003, the Company had repurchased 274,500 shares.

## ITEM 6. SELECTED FINANCIAL DATA

The following selected financial data of the Company is qualified by reference to and should be read in conjunction with the Financial Statements, including the Notes thereto, and Management's Discussion and Analysis of Financial Condition and Results of Operations included elsewhere herein. The Statement of Operations Data for each of the years in the three-year period ended June 30, 2003 and the Balance Sheet Data as of June 30, 2003 and 2002 are derived from, and are qualified by reference to, the Financial Statements included herein. The Statement of Operations Data for the years ended June 30, 2000, and 1999 and the Balance Sheet Data as of June 30, 2001, 2000 and 1999 are derived from audited financial statements not included herein. The Company's fiscal years have been shown as ending on June 30. Fiscal years 2003, 2002, 2001, 2000 and 1999 ended on June 28, 2003, June 29, 2002, June 30, 2001, July 1, 2000 and July 3, 1999, respectively.

	<b>Fiscal Year Ended June 30,</b>				
	<u>2003</u>	<u>2002</u>	<u>2001</u>	<u>2000</u>	<u>1999</u>
	(in thousands, except per share data)				
<b>Statement of Operations Data:</b>					
Net revenues	\$44,958	\$47,589	\$108,313	\$90,977	\$59,797
Cost of revenues	31,468	33,871	62,388	52,540	35,484
Gross profit	13,490	13,718	45,925	38,437	24,313
Operating expenses:					
Research and development	11,347	11,663	10,993	8,118	5,976
Selling, general and administrative	11,283	11,778	15,150	12,449	9,175
Restructuring charge	1,431	---	522	---	---
Total operating expenses	24,061	23,441	26,665	20,567	15,151
Income (loss) from operations	(10,571)	(9,723)	19,260	17,870	9,162
Other income, net	3,477	1,494	7,888	3,263	1,098
Income (loss) before income taxes	(7,094)	(8,229)	27,148	21,133	10,260
Income tax provision (benefit)	(2,767)	(2,962)	9,789	7,918	3,488
Net income (loss)	(\$4,327)	(\$5,267)	\$17,359	\$13,215	\$ 6,772
Basic earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.70	\$0.63	\$0.36
Diluted earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.64	\$0.56	\$0.33
Shares used in computing basic earnings (loss) per share (1)	25,721	25,339	24,914	20,906	18,790
Shares used in computing diluted earnings (loss) per share (1)	25,721	25,339	27,242	23,578	20,558

	<b>As of June 30,</b>				
	<u>2003</u>	<u>2002</u>	<u>2001</u>	<u>2000</u>	<u>1999</u>
	(in thousands)				
<b>Balance Sheet Data:</b>					
Working capital	\$158,662	\$167,517	\$168,066	\$153,273	\$37,642
Total assets	190,237	193,745	196,427	180,366	55,925
Shareholders' equity	182,323	185,722	187,190	164,772	46,380

- (1) See Note 1 of Notes to Financial Statements for an explanation of the method used to determine the number of shares used in computing basic and diluted earnings per share.

## ITEM 7. MANAGEMENT'S DISCUSSION AND ANALYSIS OF FINANCIAL CONDITION AND RESULTS OF OPERATIONS

### MANAGEMENT'S DISCUSSION AND ANALYSIS OF FINANCIAL CONDITION AND RESULTS OF OPERATIONS

This Annual Report includes "forward-looking statements" within the meaning of Section 27A of the Securities Act of 1933, as amended and Section 21E of the Securities Exchange Act of 1934, as amended. All statements other than statements of historical fact are "forward-looking statements" for purposes of these provisions, including any statements regarding projections of earnings, revenues or other financial items; plans and objectives of management for future operations; future expenditures; proposed new products or services and their development schedule; industry, technological or market trends, our ability to address the need for application specific logic products; our ability to respond rapidly to customer needs; expanding product sales; future economic conditions or performance, and any statement of assumptions underlying any of the foregoing. In some cases, forward-looking statements can be identified by the use of terminology such as "may," "will," "expects," "plans," "anticipates," "estimates," "potential," or "continue," or the negative thereof or other comparable terminology. Although we believe that the expectations reflected in the forward-looking statements contained herein are reasonable, there can be no assurance that such expectations or any of the forward-looking statements will prove to be correct, and actual results could differ materially from those projected or assumed in the forward-looking statements. Some of the factors that could cause our actual results to differ materially are set forth herein under the heading "Risk Factors" and elsewhere in this report. All forward-looking statements and reasons why results may differ included in this Annual Report are made as of the date hereof, and we assume no obligation to update any such forward-looking statement or reason why actual results may differ.

#### CRITICAL ACCOUNTING POLICIES

Our consolidated financial statements have been prepared in accordance with accounting principles generally accepted in the United States of America. The preparation of such statements requires us to make estimates and assumptions that affect the reported amounts of revenues and expenses during the reporting period and the reported amounts of assets and liabilities as of the date of the financial statements. Our estimates are based on historical experience and other assumptions that we consider to be reasonable given the circumstances. Actual results may vary from our estimates.

We consider the following accounting policies are "critical" as defined by the Securities and Exchange Commission, in that they are both highly important to the portrayal of our financial condition and results, and require us to make difficult judgements and assumptions about matters that are inherently uncertain. We also have other important policies that are discussed in Note 1 to the Consolidated Financial Statements.

**REVENUE RECOGNITION.** We recognize revenue from the sale of our products upon shipment, provided title and risk of loss has passed to the customer, the fee is fixed and determinable and collection of the revenue is reasonably assured. A provision for estimated future returns and other charges against revenue is recorded at the time of shipment. Approximately 59% of our sales are to distributors. We have agreements for price adjustment and stock rotation programs with our distributors. The revenue we record for sales to our distributors is net of estimated provisions for these programs. When determining this net revenue, we must make significant judgements and estimates. Our estimates are based on historical experience rates, inventory levels in the distribution channel, current trends and other related factors. However, because of the inherent nature of estimates, there is a risk that there could be significant differences between actual amounts and our estimates. Our financial condition and operating results depend on our ability to make reliable estimates and we believe that our estimates are reasonable.

**INVENTORIES.** Inventories are recorded at the lower of standard cost (which generally approximates actual cost on a first-in, first-out basis) or market value. We record charges for excess and obsolete inventory based on inventory age, shipment history and our forecast of demand over a specific future period of time. Actual market conditions in the volatile semiconductor markets that we serve may vary from our forecast or other assumptions,

potentially impacting our assessment of excess and obsolete inventory resulting in material effects on our gross margin.

**INVESTMENTS.** Pericom adopted SFAS No. 142, Goodwill and Other Intangible Assets, for its fiscal year beginning July 1, 2001. Accordingly, we ceased amortizing goodwill with a net carrying value of \$1,323,000 and annual amortization of \$338,000 that resulted from business combinations initiated prior to the adoption of SFAS No. 141, Business Combinations. SFAS No. 142 also requires that goodwill be tested for impairment at least annually. We determined that no impairment of this goodwill existed in fiscal 2003, and we will continue to evaluate such goodwill at least annually.

We also made other investments in fiscal 2003 including bridge loans convertible to equity as well as direct equity investments. These investments are included in other long-term assets in our balance sheet and are carried at the lower of cost, or market if the investment has experienced an other than temporary decline in value. We monitor these investments quarterly and make appropriate reductions in carrying value if a decline in value is deemed to be other than temporary.

## OVERVIEW

Pericom Semiconductor Corporation was incorporated in June 1990. We completed our first profitable fiscal year on June 30, 1993. We design, manufacture and market high performance digital, analog and mixed-signal integrated circuits used for the transfer, routing, and timing of digital and analog signals within and between computer, networking, data communications and telecommunications systems. Our first volume sales occurred in fiscal 1993 and consisted exclusively of 5-volt 8-bit interface logic circuits. Since then we have expanded our product offering by introducing the following products, among others:

- In fiscal 1994, 3.3-volt 16-bit logic circuits and 8-bit digital switches;
- in fiscal 1995, clock generators, 3.3-volt clock synthesizers and buffers, and high-speed interface products for the networking industry in fiscal 1995;
- in fiscal 1996, 32-bit logic, 16-bit digital switches and Pentium, 56K modem and laser printer clock synthesizers;
- in fiscal 1997, an analog switch family, mixed-voltage logic and a family of clock generators;
- in fiscal 1998, a family of advanced 3.3-volt CMOS logic; timing devices for Pentium and Pentium II mobile computers; a complete solution for the PC100 memory module standard; and a 3.3-volt bus switch family offering the fastest bus switches on the market;
- in fiscal 1999, three families of 2.5-volt zero-delay clock drivers for the networking and telecommunications markets; a family of application-specific bus switches and integrated clock generators to support the latest Intel processors and a complete interface solution for the PC133 memory module standard;
- in fiscal 2000, a crossbar switch for backplane applications like server clusters, networking, industrial computing and Storage Area Networks; a family of drivers, receivers, and transceivers supporting the "Low Voltage Differential Signaling" interface standard; the "SuperClock" programmable skew timing product family targeting networking, telecommunications and Storage Area Network applications; a complete interface solution for the new double data rate (DDR) SDRAM memory standard; a family of LVDS cross-point switches to route signals in OC12 networks; and the AVC+ family of high speed interface logic;
- in fiscal 2001, a 2.5 volt switch products and complementary complete timing interface solution for use in Double Data Rate (DDR) synchronous DRAM modules; LVDS products for the networking market

including both dual and quad crosspoint switches for both point-to-point and bus communications; several "hot-plug" switches that support the growing demand for 24/7 operations; expansion of the 2.5 volt AVC family; and low voltage bus switch products that provide 3.3 volt to 2.5 volt and 2.5 volt to 1.8 volt level translation;

- In fiscal 2002, advanced AVC products that allow voltage shifting (between 3.3 volts and 1.5 Volts) and I/O translation (from HSTL to LVTTTL) commonly needed between the latest state of the art ASICs and other legacy products; enhancements to our DDR memory module solutions; Active Termination Clamp that help to clean up signaling overshoot common at fast clock rates; additional family of seven analog multiplexers and switches with improved performance, which are used in a wide range of space constrained mobile and handheld computing applications; and a family of 25 single, dual, and triple gate logic devices in extremely small packaging that address a very broad range of applications and systems requirements; and
- In fiscal 2003, a family of Intel Compatible PCI-Bridge Products that offer higher margins; 5V low R-on 1 Ohm General Purpose Switches; support for DDR I at 333MHz and 400Mhz plus our first DDR II products; Gigabit LAN Switch used in Notebook computers; VCXO for Set Top Boxes and DSL products; and we expanded our standard logic product line.

As is typical in the semiconductor industry, we expect selling prices for our products to decline over the life of each product. Our ability to increase net revenues is highly dependent upon our ability to increase unit sales volumes of existing products and to introduce and sell new products in quantities sufficient to compensate for the anticipated declines in selling prices of existing products. We seek to increase unit sales volume through increased wafer fabrication capacity allocations from our existing foundries, qualification of new foundries, increased number of die per wafer through die size reductions and improved yields of good die through the implementation of advanced process technologies, but there can be no assurance that we will be successful in these efforts. In fiscal 2003, 2002 and 2001, respectively, approximately 72%, 68%, and 59% of the wafers for our semiconductor products were manufactured by Chartered Semiconductor Manufacturing. We qualified AMS as a wafer supplier in fiscal 1991, NJRC in fiscal 1995, TSMC in fiscal 1997 and Hyundai/Hynix in 1998.

Declining selling prices will adversely affect gross margins unless we are able to offset such declines with the sale of new higher margin products or achieve commensurate reductions in unit costs. We seek to improve our overall gross margin through the development and introduction of selected new products that we believe will ultimately achieve higher gross margins. A higher gross margin for a new product is typically not achieved until some period after the initial introduction of the product -- after start-up expenses for that product have been incurred and once volume production begins. In general, costs are higher at the introduction of a new product due to the use of a more generalized design schematic, lower economies of scale in the assembly phase and lower die yield. Our ability to decrease unit cost depends on our ability to shrink the die sizes of our products, improve yields, obtain favorable subcontractor pricing, and make in-house manufacturing operations more productive and efficient. There can be no assurance that these efforts, even if successful, will be sufficient to offset declining selling prices.

## RESULTS OF OPERATIONS

The following table sets forth certain statement of operations data as a percentage of net revenues for the periods indicated.

	Fiscal Year Ended		
	2003	June 30, 2002	2001
Net revenues	100.0%	100.0%	100.0%
Cost of revenues	70.0	71.2	57.6
Gross margin	30.0	28.8	42.4
Operating expenses:			
Research and development	25.2	24.5	10.1
Selling, general and administrative	25.1	24.7	14.0
Restructuring charge	3.2	---	0.5
Total operating expenses	53.5	49.2	24.6
Income (loss) from operations	(23.5)	(20.4)	17.8
Other income, net	7.7	3.1	7.3
Income (loss) before income taxes	(15.8)	(17.3)	25.1
Income tax provision (benefit)	(6.2)	(6.2)	9.1
Net income (loss)	(9.6%)	(11.1%)	16.0%

## COMPARISON OF FISCAL 2003, 2002 AND 2001

**NET REVENUES.** Net revenues decreased 5.5% from \$47.6 million in fiscal 2002 to \$45.0 million in fiscal 2003. The decrease in net revenues resulted primarily from a rapid decline in weighted average selling prices and continued reductions of inventory levels in the distribution and contract manufacturing sales channels. Net revenues decreased 56.0% from \$108.3 million in fiscal 2001 to \$47.6 million in fiscal 2002. The decrease in net revenues from fiscal 2001 to fiscal 2002 resulted primarily from the prolonged worldwide semiconductor industry downturn and a decline in the weighted average selling prices of our products. The rapid semiconductor industry downturn that began in fiscal 2001 also caused an over-supply of inventories in the global distribution and contract manufacturing sales channels. This inventory imbalance pushed order rates down, caused a corresponding drop in backlog and caused the percentage of net sales represented by orders placed and shipped in the same quarter to grow. These orders are called "turns" orders. With reduced backlog and increased reliance on turns orders, there is more uncertainty in the Company's ability to predict near-term revenue levels.

**GROSS PROFIT.** Gross profit decreased slightly from \$13.7 million in fiscal 2002 to \$13.5 million in fiscal 2003. Gross margin increased slightly from 28.8% in fiscal 2003 to 30.0% in fiscal 2003. The slight increase in gross margin was largely due to significant nonrecurring inventory write-downs recorded in fiscal 2002. The level of inventory reserves recorded in fiscal 2003 was significantly less than in fiscal 2002. Excluding the inventory reserve charges in fiscal 2002, product margins actually declined from fiscal 2002 to fiscal 2003 due primarily to declines in weighted average selling prices. Gross profit decreased 70.2% from \$46.0 million in fiscal 2001 to \$13.7 million in fiscal 2002. Gross margin decreased from 42.4% in fiscal 2001 to 28.8% in fiscal 2002. The decrease in gross margin resulted from decreases in the average selling prices of our various product lines and from under-absorption of factory overhead due to reduced unit volumes. These decreases were partially offset by cost reductions achieved through reduced wafer, assembly and test costs. Future gross profit and gross margins are highly dependent on the amount and product mix of turns orders. Because of this, the Company is not able to predict future gross profit levels or gross margins with certainty.

**RESEARCH AND DEVELOPMENT.** Research and development expenses decreased 3.4% from \$11.7 million in fiscal 2002 to \$11.3 million in fiscal 2003. Research and development expenses increased 6.4% from \$11.0 million in fiscal 2001 to \$11.7 million in fiscal 2002. As a percentage of net revenues, research and development increased from 24.5% in fiscal 2002 to 25.2% in fiscal 2003. In 2003 we continued our commitment to new

product development and at the same time realized certain efficiencies that resulted in a slight decrease in absolute dollars of research and development spending as compared with 2002. As a percentage of net revenues, research and development increased from 10.1% in fiscal 2001 to 24.5% in fiscal 2002. The increase in absolute dollars in research and development spending from fiscal 2001 to fiscal 2002 was primarily attributable to development costs for new products in each of our product lines and, to a lesser extent, expansion of engineering staff and related infrastructure. We believe that continued investment in research and development is critical to attaining our strategic objectives, and as a result we expect research and development expenses to increase in absolute dollars in future periods.

**SELLING, GENERAL AND ADMINISTRATIVE.** Selling, general and administrative expenses consist primarily of personnel and related overhead costs for sales, marketing, finance, human resources and general management. Selling, general and administrative expenses decreased 4.2% from \$11.8 million in fiscal 2002 to \$11.3 million in fiscal 2003 but increased as a percentage of sales from 24.7% in fiscal 2002 to 25.1% in fiscal 2003. The expense decrease was primarily due to decreased staffing levels. Selling, general and administrative expenses decreased 22.4% from \$15.2 million in fiscal 2001 to \$11.8 million in fiscal 2002, but increased as a percentage of sales from 14.0% in fiscal 2001 to 24.7% in fiscal 2002. The expense decrease was primarily due to decreased staffing levels and decreases in commissions and other incentives resulting from the decline in sales and profitability.

The Company anticipates that selling, general and administrative expenses will increase in future periods over the long term due to increased staffing levels, particularly in sales and marketing, as well as increased commission expense to the extent the Company achieves higher sales levels. In the short term, the Company intends to continue to focus on controlling costs until business conditions improve. If business conditions deteriorate or the rate of improvement does not meet our goals, the Company may implement further cost-cutting actions.

**RESTRUCTURING CHARGE.** In fiscal 2003 there was a restructuring charge of \$1.4 million related to an unused leased facility. This restructuring charge is sufficient to cover the costs of the facility through the term of the lease. There was also a restructuring charge of \$522,000 in fiscal 2001 related to the same facility. There were no restructuring charges in fiscal 2002.

**OTHER INCOME, NET.** Other income, net includes interest income and expense, investment write-offs and our allocated portion of net losses of Pericom Technology, Inc. ("PTI"), a British Virgin Islands corporation based in Shanghai, People's Republic of China. PTI was formed by Pericom and certain Pericom shareholders in 1994 to develop and market semiconductors in China and certain other Asian countries (see Note 4 of Notes to Financial Statements). Other income, net increased from \$1.5 million in fiscal 2002 to \$3.5 million in fiscal 2003. Interest income decreased from \$5.6 million in fiscal 2002 to \$5.2 million in fiscal 2003 primarily due to declines in short term interest rates. Our share in the net losses of PTI decreased from \$1.4 million in fiscal 2002 to \$739,000 in fiscal 2003 as the market conditions in China have improved. In addition, we wrote off investments totaling \$1.0 million in fiscal 2003. Other income, net decreased from \$7.9 million in fiscal 2001 to \$1.5 million in fiscal 2002. Interest income decreased from \$8.3 million in fiscal 2001 to \$5.6 million in fiscal 2002 primarily due to declines in short-term interest rates. Our share in the net losses of PTI increased from \$68,000, excluding \$331,000 in goodwill amortization, in fiscal 2001 to \$1.4 million in fiscal 2002 as PTI's losses increased due to the global semiconductor industry downturn. In addition, we wrote off \$2.7 million in investments in fiscal 2002. No investments were written off in fiscal 2001.

**PROVISION FOR INCOME TAXES.** The income tax benefit in fiscal 2003 was \$2.8 million as compared with a benefit of \$3.0 million in fiscal 2002. The income tax benefit in fiscal 2002 was \$3.0 million as compared with a provision of \$9.8 million in fiscal 2001. The income tax benefit in fiscal 2002 and fiscal 2003 were the result of the carry back of the net loss in each year to prior fiscal years. The provision for income taxes differed from the federal statutory rate primarily due to state income taxes, the utilization of research and development tax credits and a change in the deferred asset valuation allowance.

## LIQUIDITY AND CAPITAL RESOURCES

Operating activities provided approximately \$2.2 million in cash in fiscal 2003, \$9.1 million in cash in fiscal 2002 and \$19.4 million in fiscal 2001. The decrease in cash flows provided by operating activities in fiscal 2003 from fiscal 2002 was primarily due to inventories increasing by \$624,000 in fiscal 2003 while inventories decreased \$5.1 million in fiscal 2002. The decrease in cash flows provided by operating activities in fiscal 2002 from fiscal 2001 was primarily due to the net loss of \$5.3 million for fiscal 2002 compared to net income of \$17.4 million for fiscal 2001. For fiscal 2003 our cash flows provided by operating activities were the result of the net loss of \$4.3 million as offset by depreciation and amortization of \$3.9 million, investment write offs of \$1.2 million, our share of the net loss of PTI of \$739,000, a decrease of \$1.6 million in accounts receivable, an increase of \$624,000 in inventory and an increase in accrued liabilities of \$904,000.

Net cash used for investing activities was \$4.7 million, \$87.4 million and \$57.0 million in fiscal 2003, 2002 and 2001, respectively. The decrease in net cash used for investing activities from fiscal 2002 to fiscal 2003 was primarily due to net purchases of short-term investments decreasing from a net purchase of \$81.0 million in fiscal 2002 to a net maturity of \$5.4 million in fiscal 2003. The increase in net cash used for investing activities from fiscal 2001 to 2002 was primarily due to net purchases of short-term investments increasing from \$45.5 million in fiscal 2001 to \$81.0 million in fiscal 2002. We made capital expenditures of approximately \$1.8 million in fiscal 2003, \$2.2 million in fiscal 2002 and \$5.8 million in fiscal 2001.

Financing activities used \$514,000 in cash in fiscal 2003 and provided approximately \$1.9 million in cash in fiscal 2002 and \$2.5 million in fiscal 2001. In fiscal 2003, net cash used in financing activities resulted from the repurchase of common stock offset in part by the issuance of common stock from employee stock plans. In fiscal 2002 and 2001, cash flows from financing activities resulted from the issuance of common stock, from employee stock plans.

Our cash, cash equivalents and short-term investments decreased by \$6.8 million to \$149.0 million on June 30, 2003 from \$155.8 million on June 30, 2002. The decrease was primarily due to our investment in Saronix LLC. We believe that cash generated from operations and existing cash balances will be sufficient to fund necessary purchases of capital equipment and leasehold improvements, to fund our repurchase of common stock and to provide working capital at least through the next 12 months. However, there can be no assurance that future events will not require us to seek additional capital sooner or, if so required, that adequate capital will be available at all or on terms acceptable to us.

Our Board of Directors has approved the repurchase of up to 2,000,000 shares of common stock. As of September 8, 2003, we have repurchased 274,500 shares at a cost of approximately \$2.2 million.

A portion of our cash may be used to acquire or invest in complementary businesses or products or to obtain the right to use complementary technologies. From time to time, in the ordinary course of business, we may evaluate potential acquisitions of such businesses, products or technologies. For example, we have signed a definitive purchase option agreement to acquire the net assets of privately held Saronix LLC. The purchase price is expected to be under \$10 million, much of which has been advanced to Saronix in the form of convertible secured loans.

## CONTRACTURAL OBLIGATIONS AND COMMITMENTS

The following table depicts our contractual obligations as of June 30, 2003 (in thousands):

<u>Contractual obligation</u>	<u>Payments Due by Period</u>			
	<u>Total</u>	<u>Less than 1 Year</u>	<u>1-3 Years</u>	<u>Thereafter</u>
Operating leases	\$ 2,634	\$ 1,752	\$ 882	\$ ---
Total contractual cash obligation	\$ 2,634	\$ 1,752	\$ 882	\$ ---

The Company leases certain facilities under operating leases. The Company has no debt, no capital leases and no purchase obligations as of June 30, 2003.

## RECENTLY ISSUED ACCOUNTING STANDARDS

In June 2001, the FASB issued SFAS No. 142, "Goodwill and Other Intangible Assets". SFAS No. 142 addresses the initial recognition and measurement of intangible assets acquired outside of a business combination and the accounting for goodwill and other intangible assets subsequent to their acquisition. SFAS No. 142 provides that intangible assets with finite useful lives be amortized and that goodwill and intangible assets with indefinite lives will not be amortized, but will rather be tested at least annually for impairment. Pericom adopted SFAS No. 142 for its fiscal year beginning July 1, 2001. On that date, we ceased amortizing goodwill with a net carrying value of \$1,323,000 at June 30, 2001 and annual amortization of \$338,000 that resulted from business combinations initiated prior to the adoption of SFAS 141, Business Combinations. We determined that no impairment of this goodwill existed in fiscal 2003. We will continue to evaluate such goodwill at least annually.

	<u>Years Ended June 30,</u>		
	<u>(In thousands, except per share amounts)</u>		
	<u>2003</u>	<u>2002</u>	<u>2001</u>
Reported net income (loss)	(\$4,327)	(\$5,267)	\$17,359
Add: Goodwill amortization	---	---	331
Adjusted net income (loss)	(\$4,327)	(\$5,267)	\$17,690
Reported basic earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.70
Add: Goodwill amortization per basic share	---	---	.01
Adjusted basic earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.71
Reported diluted earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.64
Add: Goodwill amortization per diluted share	---	---	.01
Adjusted diluted earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.65

In August 2001, the FASB issued SFAS No. 144, "Accounting for the Impairment or Disposal of Long-Lived Assets". SFAS No. 144 addresses financial accounting and reporting for the impairment or disposal of long-lived assets. It superseded SFAS No. 121, Accounting for the Impairment of Long-Lived Assets and for Long-Lived Assets to be Disposed of, and the accounting and reporting provisions of Accounting Principles Board No. 30, Reporting the Results of Operations – Reporting the Effects of Disposal of a Segment of a Business and Extraordinary, Unusual and Infrequently Occurring Events and Transactions for the disposal of a segment of a business. We adopted SFAS No. 144 on July 1, 2002. The adoption of SFAS No. 144 had no effect on our results of operations, financial position or liquidity.

In April 2002, the FASB issued SFAS No. 145, "Rescission of FASB Statement No. 4, 44, and 64, Amendment of FASB Statement No. 13, and Technical Corrections". SFAS 145 requires that any gains or losses on extinguishment of debt that were classified as an extraordinary item in prior periods that are not unusual in nature and infrequent in occurrence be reclassified to other income (expense), beginning in fiscal 2003, with early adoption encouraged. We adopted SFAS No. 145 on July 1, 2002. The adoption of SFAS No. 145 had no effect on our results of operations or financial position.

In June 2002, the FASB issued SFAS No. 146, "Accounting for Costs Associated with Exit or Disposal Activities", which addresses accounting for restructuring and similar costs. SFAS No. 146 supersedes previous accounting guidance, principally Emerging Issues Task Force Issue No. 94-3. SFAS No. 146 requires that the liability for costs associated with an exit or disposal activity be recognized when the liability is incurred. Under Issue 94-3, a liability for an exit cost was recognized at the date of our commitment to an exit plan. SFAS No. 146 also establishes that the liability should initially be measured and recorded at fair value. Accordingly, SFAS No. 146 may affect the timing of recognizing future restructuring costs as well as the amounts recognized. We adopted SFAS No. 146 on January 1, 2003. The adoption of SFAS No. 146 had no effect on our results of operations or financial position.

In November 2002, the FASB issued FASB Interpretation No. (FIN) 45, "Guarantor's Accounting and Disclosure Requirements for Guarantees, Including Indirect Guarantees of Indebtedness of Others." FIN 45 requires that upon issuance of a guarantee, the guarantor must recognize a liability for the fair value of the obligation it assumes under that guarantee. The provisions for initial recognition and measurement are effective on a prospective basis for guarantees that are issued or modified after December 31, 2002, irrespective of a guarantor's year-end. The disclosure requirements of FIN 45 were effective for interim and annual periods ending after December 15, 2002, and are applicable to our product warranty liability and other guarantees. We adopted the provisions of FIN 45 for guarantees issued or modified after December 31, 2002, and the adoption of FIN 45 did not have a material impact on our results of operations or financial position.

In December 2002, the FASB issued SFAS 148, "Accounting for Stock-Based Compensation — Transition and Disclosure," which amends SFAS 123, "Accounting for Stock-Based Compensation." SFAS 148 provides alternative methods of transition for an entity that voluntarily changes to the fair value based method of accounting for stock-based employee compensation. It also amends the disclosure provisions of SFAS 123 to require prominent disclosure about the effects an entity's accounting policy decisions with respect to stock-based employee compensation on reported net income in both annual and interim periods. SFAS 148 is effective for annual periods ending after December 15, 2002 and interim periods beginning after December 15, 2002. We adopted the disclosure requirements of SFAS 148 effective March 31, 2003 and continue to account for stock based compensation under APB Opinion 25 and related Interpretations. The adoption of the disclosure requirements of SFAS 148 did not have a material impact on our results of operations or financial position.

In January 2003, the FASB issued FIN 46, "Consolidation of Variable Interest Entities." FIN 46 establishes accounting guidance for consolidation of a variable interest entity (VIE), formerly referred to as special purpose entities. FIN 46 applies to any business enterprise, both public and private, that has a controlling interest, contractual relationship or other business relationship with a VIE. FIN 46 provides guidance for determining when an entity (the Primary Beneficiary) should consolidate a VIE that functions to support the activities of the Primary Beneficiary. We have no contractual relationship or other business relationship with a VIE, and therefore the adoption will not have an effect on our consolidated results of operations or financial position.

## **ITEM 7A. QUANTITATIVE AND QUALITATIVE DISCLOSURES ABOUT MARKET RISK**

### **MARKET RISK DISCLOSURE**

At June 30, 2003, our investment portfolio consisted of fixed income securities, excluding those classified as cash equivalents, of \$139.3 million (see Note 1 of Notes to Financial Statements). These securities are subject to interest rate risk and will decline in value if market interest rates increase. Due to the short duration and conservative nature of these instruments, we do not believe that we have a material exposure to interest rate risk. For example, if market interest rates were to increase immediately and uniformly by 10% from levels as of June

30, 2003, the decline in the fair value of the portfolio would not have a material effect on our results of operations over the next fiscal year.

**ITEM 8. FINANCIAL STATEMENTS AND SUPPLEMENTARY DATA**

**1. INDEX TO CONSOLIDATED FINANCIAL STATEMENTS**

The following Consolidated Financial Statements are filed as part of this report.

	Page No.
Independent Auditors' Report.....	38
Consolidated Balance Sheets as of June 30, 2003 and 2002.....	39
Consolidated Statements of Operations for each of the three fiscal years in the period ended June 30, 2003.....	40
Consolidated Statements of Shareholders' Equity and Comprehensive Income for each of the three fiscal years in the period ended June 30, 2003.....	41
Consolidated Statements of Cash Flows for each of the three fiscal years in the period ended June 30, 2003 .....	42
Notes to Consolidated Financial Statements.....	43

**2. INDEX TO SUPPLEMENTAL FINANCIAL INFORMATION**

Unaudited Interim Financial Information.....	57
--	----

**3. INDEX TO FINANCIAL STATEMENT SCHEDULE**

The following financial statement schedule of Pericom Semiconductor Corp. for the years ended June 30, 2003, 2002, and 2001 is filed as part of this report and should be read in conjunction with the Financial Statements of Pericom Semiconductor Corp.

Schedule II – Valuation and Qualifying Accounts for each of the three fiscal years in the period ended June 30, 2003.....	59
--	----

---

Schedules other than that listed above have been omitted since they are either not required, not applicable, or the information is otherwise included.

**ITEM 9. CHANGES IN AND DISAGREEMENTS WITH ACCOUNTANTS ON ACCOUNTING AND FINANCIAL DISCLOSURE**

Not Applicable.

**ITEM 9A. CONTROLS AND PROCEDURES**

- A) We carried out an evaluation, under the supervision and with the participation of our management, including our President and Chief Executive Officer along with our Chief Financial Officer, of the effectiveness of the design and operation of our disclosure controls and procedures as of the end of our fourth fiscal quarter pursuant to Exchange Act Rule 13a-15(b). Based upon that evaluation, our President and Chief Executive Officer along with our Chief Financial Officer concluded that our disclosure controls and procedures are effective in timely alerting them to material information relating to Pericom (including its consolidated subsidiaries) required to be included in our periodic SEC filings.
  
- B) There have been no changes in our internal control over financial reporting identified in connection with our evaluation that occurred during our fourth fiscal quarter that have materially affected, or are reasonably likely to materially affect, our internal control over financial reporting.

### PART III

#### ITEM 10. DIRECTORS AND EXECUTIVE OFFICERS OF THE REGISTRANT

The information required by this item is incorporated by reference to the section captioned "Election of Directors" contained in the Proxy Statement.

#### ITEM 11. EXECUTIVE COMPENSATION

The information required by this item is incorporated by reference to the section captioned "Executive Compensation" contained in the Company's Definitive Proxy Statement related to the Annual Meeting of Shareholders to be held December 17, 2003, to be filed by the Company with the Securities and Exchange Commission (the "Proxy Statement").

#### ITEM 12. SECURITY OWNERSHIP OF CERTAIN BENEFICIAL OWNERS AND MANAGEMENT

The information required by this item is incorporated by reference to the section captioned "Security Ownership of Certain Beneficial Owners and Management" contained in the Proxy Statement.

#### EQUITY COMPENSATION PLANS

The following table summarizes share and exercise price information about our equity compensation plans as of June 30, 2003.

Plan Category	Number of Securities to be Issued Upon Exercise of Outstanding Options, Warrants and Rights	Weighted Average Exercise Price of Outstanding Options, Warrants and Rights	Number of Securities Remaining Available For Future Issuance Under Plans
Equity Compensation Plans Approved by Shareholders			
Option Plans	5,552,412	\$11.36	720,630
Employee Stock Purchase Plan	---	---	334,204

Note: Pericom has no equity compensation plans not approved by shareholders.

#### ITEM 13. CERTAIN RELATIONSHIPS AND RELATED TRANSACTIONS

The information required by this item is incorporated by reference to the section captioned "Certain Transactions" contained in the Proxy Statement.

#### ITEM 14. PRINCIPAL ACCOUNTANT FEES AND SERVICES

Pursuant to SEC Release No. 33-8183 (as amended by release No. 33-8183A), the disclosure requirements of this Item are not effective until the Annual Report on Form 10-K for the Company's first fiscal year ending after December 15, 2003.

## PART IV

### ITEM 15. EXHIBITS, FINANCIAL STATEMENT SCHEDULES, AND REPORTS ON FORM 8-K

- (a) The following documents are filed as part of this report:
- (1) Financial Statements and Financial Statement Schedule – See Index to Financial Statements and Financial Statement Schedule at Item 8 on Page 33 of this report.
  - (2) Exhibits. The following exhibits are filed as part of, or incorporated by reference into, this Report:
    - 3.1 Restated Articles of Incorporation of the Registrant (1)
    - 3.2 Amended and Restated Bylaws of the Registrant (2)
    - 3.3 Certificate of Determination of the Series D Junior Participating Preferred Shares. Incorporated by reference to Exhibit 3.1 to the Registrant's Quarterly Report on Form 10-Q for the quarter ended March 30, 2002
    - 4.1 Rights Agreement between Pericom Semiconductor Corporation and Equiserve Trust Company, N.A. dated as of March 6, 2002 including Form of Right Certificate attached thereto as Exhibit B. Incorporated by reference to Exhibit 4 to Registration Statement on Form 8-A filed by the Registrant dated March 12, 2002
    - 10.1 Registrant's 1990 Stock Option Plan, including Forms of Agreements thereunder (3)
    - 10.2 Registrant's 1995 Stock Option Plan, including Forms of Agreements thereunder (3)
    - 10.3 Registrant's 1997 Employee Stock Purchase Plan, including Forms of Agreements thereunder (3)
    - 10.4 Lease, dated November 29, 1993, by and between Orchard Investment Company Number 510 as Landlord and Registrant as Tenant, as amended (3)
    - 10.5 Third Amendment to Lease, dated April 23, 1999, by and between CarrAmerica Realty Corporation as Landlord and Registrant as Tenant (2)
    - 10.6 Fourth Amendment to Lease, dated January 21, 2000, by and between CarrAmerica Realty Corporation as Landlord and Registrant as Tenant (4)
    - 10.7 Fifth Amendment to Lease, dated May 1, 2000, by and between CarrAmerica Realty Corporation as Landlord and Registrant as Tenant (4)
    - 10.8 Sixth Amendment to Lease, dated October 31, 2000, by and between CarrAmerica Realty Corporation as Landlord and Registrant as Tenant (6)
    - 10.11 Form of Indemnification Agreement (3)
    - 10.12 Pericom Technology Agreement, dated March 17, 1995 by and between the Registrant and Pericom Technology, Inc. (3)
    - 10.13 Registrant's 2000 Employee Stock Purchase Plan, including forms of Agreement thereunder (5)
    - 10.14 Registrant's 2001 Stock Incentive Plan, including forms of Agreement thereunder (5)
    - 10.15 Sublease of 2249 Zanker Road, San Jose, California. Incorporated by reference to the Company's Quarterly report on Form 10-Q for the quarter ended September 29, 2001 in which the exhibit bears the same number
    - 10.16 Change of Control Agreement (7)
    - 23.1 Consent of Independent Auditors
    - 31.1 Certification of Alex C. Hui, Chief Executive Officer, pursuant to Section 302 of the Sarbanes-Oxley Act of 2002
    - 31.2 Certification of Michael D. Craighead, Chief Financial Officer, pursuant to Section 302 of the Sarbanes-Oxley Act of 2002
    - 32.1 Certification of Alex C. Hui, Chief Executive Officer, pursuant to Section 906 of the Sarbanes-Oxley Act of 2002
    - 32.2 Certification of Michael D. Craighead, Chief Financial Officer, pursuant to Section 906 of the Sarbanes-Oxley Act of 2002

- (1) Incorporated herein by reference to the Company's Quarterly Report on Form 10-Q for the quarter ended March 31, 2001 in which the exhibit bears the same number.
- (2) Incorporated herein by reference to the Company's fiscal 1999 Annual Report on Form 10-K in which the exhibit bears the same number.
- (3) Incorporated herein by reference to the Company's Registration Statement on Form S-1 in which the exhibit bears the same number.
- (4) Incorporated herein by reference to the Company's fiscal 2000 Annual Report on Form 10-K in which the exhibit bears the same number.
- (5) Incorporated herein by reference to the Company's Quarterly Report on Form 10-Q for the quarter ended December 30, 2000 in which the exhibit bears the same number.
- (6) Incorporated herein by reference to the Company's fiscal 2001 Annual Report on Form 10-K in which the exhibit bears the same number.
- (7) Incorporated herein by reference to the Company's fiscal 2002 Annual Report on Form 10-K in which the exhibit bears the same number.

(b) Reports on Form 8-K:

- (1) On April 22, 2003, Pericom filed a report on Form 8-K regarding the quarterly and year to date results press release issued the same day.
- (2) On July 29, 2003, Pericom filed a report on Form 8-K regarding the quarterly and year to date results press release issued the same day.

(c) Exhibits: See list of exhibits under (a)(2) above.

(d) Financial Statement Schedules: See list of schedules under (a)(1) above.

## INDEPENDENT AUDITORS' REPORT

To the Board of Directors and Shareholders of  
Pericom Semiconductor Corporation:

We have audited the accompanying balance sheets of Pericom Semiconductor Corporation (the "Company") as of June 30, 2003 and 2002, and the related statements of operations, shareholders' equity and comprehensive income, and cash flows for each of the three years in the period ended June 30, 2003. These financial statements are the responsibility of the Company's management. Our responsibility is to express an opinion on these financial statements based on our audits.

We conducted our audits in accordance with auditing standards generally accepted in the United States of America. Those standards require that we plan and perform the audit to obtain reasonable assurance about whether the financial statements are free of material misstatement. An audit includes examining, on a test basis, evidence supporting the amounts and disclosures in the financial statements. An audit also includes assessing the accounting principles used and significant estimates made by management, as well as evaluating the overall financial statement presentation. We believe that our audits provide a reasonable basis for our opinion.

In our opinion, such financial statements present fairly, in all material respects, the financial position of Pericom Semiconductor Corporation at June 30, 2003 and 2002, and the results of its operations and its cash flows for each of the three years in the period ended June 30, 2003, in conformity with accounting principles generally accepted in the United States of America.

Our audits were conducted for the purpose of forming an opinion on the basic financial statements taken as a whole. The financial statement schedule listed in Item 14(a)(2) is presented for the purpose of additional analysis and is not a required part of the basic financial statements. This financial statement schedule is the responsibility of the Company's management. Such financial statement schedule has been subjected to the auditing procedures applied in our audits of the basic financial statements and, in our opinion, is fairly stated in all material respects when considered in relation to the basic financial statements taken as a whole.

*Deloitte & Touche LLP*

San Jose, California  
July 23, 2003

**PERICOM SEMICONDUCTOR CORPORATION**

**CONSOLIDATED BALANCE SHEETS**

(In thousands, except share data)

	<b>June 30,</b>	
	<b>2003</b>	<b>2002</b>
<b>ASSETS</b>		
Current assets:		
Cash and cash equivalents	\$ 9,705	\$ 12,656
Short-term investments	139,285	143,156
Accounts receivable:		
Trade (net of allowances of \$3,546 and \$4,376)	2,502	3,981
Other	1,800	1,871
Inventories	9,963	9,339
Prepaid expenses and other current assets	1,301	1,298
Deferred income taxes	1,401	2,600
Total current assets	165,957	174,901
Property and equipment—net	6,305	8,608
Investment in and advances to joint venture	5,181	5,920
Goodwill	1,325	1,325
Deferred income taxes—non current	1,085	---
Loans to Saronix LLC	7,003	---
Other assets	3,381	2,991
Total	\$ 190,237	\$ 193,745
<b>LIABILITIES AND SHAREHOLDERS' EQUITY</b>		
Current liabilities:		
Accounts payable	\$ 4,170	\$ 4,544
Accrued liabilities	3,125	2,840
Total current liabilities	7,295	7,384
Other long term liabilities	619	---
Deferred income taxes	---	639
Commitments and contingencies (Notes 8 and 9)		
Shareholders' equity:		
Common stock, 60,000,000 shares authorized;		
Shares outstanding: 2003, 25,765,177; 2002, 25,660,209	139,401	139,345
Accumulated other comprehensive income	1,129	257
Retained earnings	41,793	46,120
Total shareholders' equity	182,323	185,722
Total	\$ 190,237	\$ 193,745

See notes to consolidated financial statements.

**PERICOM SEMICONDUCTOR CORPORATION**  
**CONSOLIDATED STATEMENTS OF OPERATIONS**

(In thousands, except per share amounts)

	<u>Years Ended June 30,</u>		
	<u>2003</u>	<u>2002</u>	<u>2001</u>
Net revenues	\$ 44,958	\$ 47,589	\$ 108,313
Cost of revenues	31,468	33,871	62,388
Gross profit	<u>13,490</u>	<u>13,718</u>	<u>45,925</u>
Operating expenses:			
Research and development	11,347	11,663	10,993
Selling, general and administrative	11,283	11,778	15,150
Restructuring charge	1,431	---	522
Total	<u>24,061</u>	<u>23,441</u>	<u>26,665</u>
Income (loss) from operations	(10,571)	(9,723)	19,260
Equity in net loss of joint venture and related goodwill amortization	(739)	(1,410)	(399)
Interest income	5,243	5,554	8,287
Other than temporary decline in the value of investments	(1,027)	(2,650)	---
Income (loss) before income taxes	<u>(7,094)</u>	<u>(8,229)</u>	<u>27,148</u>
Income tax provision (benefit)	(2,767)	(2,962)	9,789
Net income (loss)	<u>(\$ 4,327)</u>	<u>(\$ 5,267)</u>	<u>\$ 17,359</u>
Basic earnings (loss) per share	<u>(\$0.17)</u>	<u>(\$0.21)</u>	<u>\$0.70</u>
Diluted earnings (loss) per share	<u>(\$0.17)</u>	<u>(\$0.21)</u>	<u>\$0.64</u>
Shares used in computing basic earnings (loss) per share	<u>25,721</u>	<u>25,339</u>	<u>24,914</u>
Shares used in computing diluted earnings (loss) per share	<u>25,721</u>	<u>25,339</u>	<u>27,242</u>

See notes to consolidated financial statements.

PERICOM SEMICONDUCTOR CORPORATION

CONSOLIDATED STATEMENTS OF SHAREHOLDERS' EQUITY AND COMPREHENSIVE  
INCOME  
(In thousands)

	<u>Common Stock</u>		<u>Retained Earnings</u>	<u>Accumulated Other Comprehensive</u>	<u>Total Shareholders' Equity</u>	<u>Comprehensive Income</u>
	<u>Shares</u>	<u>Amount</u>		<u>Income (Loss)</u>		
BALANCES, July 1, 2000	24,494	\$130,834	\$34,028	(\$90)	\$164,772	
Net income	---	---	17,359	---	17,359	\$17,359
Unrealized loss on investments	---	---	---	(114)	(114)	(114)
Comprehensive income	---	---	---	---	---	<u>\$17,245</u>
Issuance of common stock under employee stock plans	647	2,549	---	---	2,549	
Issuance of common stock options to non-employees	---	137	---	---	137	
Tax benefit resulting from stock option transactions	---	2,487	---	---	2,487	
BALANCES, June 30, 2001	25,141	136,007	51,387	(204)	187,190	
Net loss	---	---	(5,267)	---	(5,267)	(\$5,267)
Unrealized gain on investments	---	---	---	461	461	461
Comprehensive loss	---	---	---	---	---	<u>(\$4,806)</u>
Issuance of common stock under employee stock plans	519	1,862	---	---	1,862	
Stock based compensation	---	253	---	---	253	
Tax benefit resulting from stock option transactions	---	1,223	---	---	1,223	
BALANCES, June 30, 2002	25,660	139,345	46,120	257	185,722	
Net loss	---	---	(4,327)	---	(4,327)	(\$4,327)
Unrealized gain on investments	---	---	---	854	854	854
Currency translation gain	---	---	---	18	18	18
Comprehensive loss	---	---	---	---	---	<u>(\$3,455)</u>
Issuance of common stock under employee stock plans	380	1,686	---	---	1,686	
Tax benefit resulting from stock option transactions	---	570	---	---	570	
Repurchase and retirement of common stock	(275)	(2,200)	---	---	(2,200)	
BALANCES, June 30, 2003	25,765	\$139,401	\$41,793	\$1,129	\$182,323	

See notes to consolidated financial statements.

**PERICOM SEMICONDUCTOR CORPORATION**  
**CONSOLIDATED STATEMENTS OF CASH FLOWS**

(In thousands)

	<u>Years Ended June 30,</u>		
	<u>2003</u>	<u>2002</u>	<u>2001</u>
<b>CASH FLOWS FROM OPERATING ACTIVITIES:</b>			
Net income (loss)	(\$4,327)	(\$5,267)	\$17,359
Adjustments to reconcile net income (loss) to net cash provided by operating activities:			
Depreciation and amortization	3,916	4,084	3,519
Other than temporary decline in the value of investments	1,156	2,650	---
(Gain) loss on disposal of assets	(45)	3	1
Stock-based compensation	---	253	137
Equity in net loss of joint venture and related goodwill amortization	739	1,410	399
Deferred income taxes	(665)	(1,507)	(618)
Changes in assets and liabilities:			
Accounts receivable	1,550	2,294	4,243
Inventories	(624)	5,101	(1,274)
Prepaid expenses and other current assets	(3)	657	(523)
Accounts payable	(374)	376	(4,815)
Accrued liabilities	904	(907)	186
Income taxes payable	---	---	778
Net cash provided by operating activities	2,227	9,147	19,392
<b>CASH FLOWS FROM INVESTING ACTIVITIES:</b>			
Purchases of property and equipment	(1,842)	(2,222)	(5,779)
Purchase of short-term investments	(172,936)	(301,168)	(57,214)
Maturities of short-term investments	178,371	220,284	11,761
Loans to Saronix	(7,003)	---	---
Increase in other assets	(1,484)	(4,323)	(1,012)
Advances to and investments in joint venture	---	---	(4,768)
Proceeds from sale of property and equipment	212	---	32
Net cash used for investing activities	(4,682)	(87,429)	(56,980)
<b>CASH FLOWS FROM FINANCING ACTIVITIES:</b>			
Proceeds from issuance of common stock under employee stock plans	1,686	1,862	2,549
Repurchase of common stock	(2,200)	---	---
Net cash provided by (used in) financing activities	(514)	1,862	2,549
<b>EFFECT OF EXCHANGE RATE CHANGES ON CASH AND CASH EQUIVALENTS</b>			
	18	---	---
<b>NET DECREASE IN CASH AND CASHEQUIVALENTS</b>	(2,951)	(76,420)	(35,039)
<b>CASH AND CASH EQUIVALENTS:</b>			
Beginning of period	12,656	89,076	124,115
End of period	\$9,705	\$12,656	\$89,076
<b>SUPPLEMENTAL DISCLOSURE OF CASH FLOW INFORMATION:</b>			
Cash paid (received) during the period for income taxes	(\$2,097)	(\$2,081)	\$8,986
<b>SUPPLEMENTAL NONCASH FINANCING ACTIVITIES:</b>			
Tax benefit from stock option transactions	\$570	\$1,223	\$2,487

See notes to consolidated financial statements.

**PERICOM SEMICONDUCTOR CORPORATION**  
**NOTES TO CONSOLIDATED FINANCIAL STATEMENTS**  
**FISCAL YEARS ENDED JUNE 30, 2003, 2002 AND 2001**

**1. BUSINESS AND SIGNIFICANT ACCOUNTING POLICIES**

Pericom Semiconductor Corporation (the "Company") was incorporated in June 1990. The Company designs, manufactures and markets high performance digital, analog and mixed-signal integrated circuits used for the transfer, routing, and timing of digital and analog signals within and between computer, networking, datacom and telecom systems.

**FINANCIAL STATEMENT ESTIMATES** -- The preparation of financial statements in conformity with accounting principles generally accepted in the United States of America requires management to make estimates and assumptions that affect the reported amounts of assets, liabilities, revenues and expenses during the reporting period. Actual results could differ from those estimates.

**BASIS OF PRESENTATION** -- These consolidated financial statements include the accounts of Pericom Semiconductor Corporation and its two wholly owned subsidiaries, Pericom Semiconductor (HK) Limited and Pericom Taiwan Limited Corporation. Pericom Semiconductor (HK) Limited was incorporated in Hong Kong on May 31, 2000 and is engaged in circuit design services solely for Pericom Semiconductor Corporation. Pericom Taiwan Limited Corporation was incorporated in Taiwan on April 29, 2003 and is initially engaged in the design, field application engineering and marketing of IC's targeted at local Taiwanese opportunities and will engage in other circuit design services for Pericom Semiconductor Corporation. All significant intercompany balances and transactions are eliminated in consolidation.

**FISCAL PERIOD** -- The Company's fiscal years in the accompanying financial statements have been shown as ending on June 30. Fiscal years 2003, 2002, and 2001 ended on June 28, 2003, June 29, 2002 and June 30, 2001, respectively. All periods presented include 52 weeks.

**CASH EQUIVALENTS** -- The Company considers all highly liquid debt instruments purchased with a remaining maturity of three months or less to be cash equivalents. The recorded carrying amounts of the Company's cash and cash equivalents approximate their fair market value.

**SHORT-TERM INVESTMENTS** -- The Company's policy is to invest in short-term instruments with investment grade credit ratings. The Company classifies its short-term investments as "available-for-sale" securities and the cost of securities sold is based on the specific identification method. At June 30, 2003 short-term investments, and any difference between the fair market value and the underlying cost of such investments, consisted of the following (in thousands):

	<b>Amortized Cost</b>	<b>Gross Unrealized Gains</b>	<b>Gross Unrealized Losses</b>	<b>Market Value</b>
Corporate bonds and notes	\$65,907	\$934	---	\$66,841
Government securities	40,833	448	---	41,281
Asset / mortgage backed securities	30,726	437	---	31,163
	<u>\$137,466</u>	<u>\$1,819</u>	<u>---</u>	<u>\$139,285</u>

At June 30, 2002 short-term investments, and any difference between the fair market value and the underlying cost of such investments, consisted of the following (in thousands):

	<u>Amortized Cost</u>	<u>Gross Unrealized Gains</u>	<u>Gross Unrealized Losses</u>	<u>Market Value</u>
Corporate bonds and notes	\$88,935	\$ ---	\$(84)	\$88,851
Government securities	37,084	207	---	37,291
Asset backed securities	16,881	133	---	17,014
	<u>\$142,900</u>	<u>\$340</u>	<u>\$(84)</u>	<u>\$143,156</u>

At June 30, 2003, these investments had the following maturities (in thousands):

	<u>Market Value</u>
One year or less	\$53,774
Between one and three years	76,035
Greater than three years	9,476
	<u>\$139,285</u>

**INVENTORIES** are recorded at the lower of standard cost (which generally approximates actual cost on a first-in, first-out basis) or market value. Charges for excess and obsolete inventory are recorded based on inventory age, shipment history and forecasted demand over a specific future period of time.

**PROPERTY AND EQUIPMENT** are stated at cost. Depreciation and amortization are computed using the straight-line method over estimated useful lives of three to five years. Leasehold improvements are depreciated over the lesser of the lease term or the estimated useful life.

**INVESTMENT IN JOINT VENTURE** is accounted for using the equity method. The difference between the carrying value and the underlying equity in net assets of the investment will be tested for impairment at least annually (see Note 4).

**INVESTMENTS IN PRIVATELY HELD COMPANIES** are carried at the lower of cost, or market if the investment has experienced an other than temporary decline in value and are included in other long-term assets on the balance sheet.

**LONG-LIVED ASSETS** -- The Company evaluates long-lived assets for impairment whenever events or changes in circumstances indicate that the carrying amount of an asset may not be recoverable. The Company's policy is to review the recoverability of all intangible assets based upon undiscounted cash flows on an annual basis at a minimum, and in addition, whenever events or changes indicate that the carrying amount of an asset may not be recoverable.

**INCOME TAXES** -- The Company accounts for income taxes under SFAS No. 109, "Accounting for Income Taxes," which requires an asset and liability approach to recording deferred taxes.

**STOCK-BASED COMPENSATION** -- The Company accounts for stock-based awards to employees using the intrinsic value method in accordance with Accounting Principles Board Opinion No. 25, "Accounting for Stock Issued to Employees," and all of its interpretations and presents pro forma disclosures required by SFAS No. 123, "Accounting for Stock-Based Compensation".

The Company accounts for equity instruments issued to nonemployees in accordance with the provisions of SFAS No. 123 and Emerging Issues Task Force (EITF) Issue No. 96-18, "Accounting for Equity Instruments

That Are Issued to Other Than Employees for Acquiring, or in Conjunction with Selling, Goods or Services,” which requires that the fair value of such instruments be recognized as an expense over the period in which the related services are received.

SFAS No. 123, “Accounting for Stock-Based Compensation”, (SFAS 123), requires the disclosure of pro forma net income as if the Company had adopted the fair value method as of the beginning of fiscal 1996. Under SFAS 123, the fair value of stock-based awards to employees is calculated through the use of option pricing models, even though such models were developed to estimate the fair value of freely tradable, fully transferable options without vesting restrictions, which significantly differ from the terms of the Company’s stock option awards. These models also require subjective assumptions, including expected time to exercise, which greatly affect the calculated values. The Company’s calculations were made using the Black-Scholes option pricing model with the following weighted average assumptions for the Company’s stock option grants:

	<u>2003</u>	<u>2002</u>	<u>2001</u>
Expected life	5 years	5 years	5 years
Risk-free interest rate	2.57-3.18%	4.37%	5.35%
Volatility	40%-73%	64%	118%
Dividend yield	0.00%	0.00%	0.00%

The following weighted average assumptions are included in the estimated grant date fair value calculations for rights to purchase stock under the Stock Purchase Plan:

	<u>2003</u>	<u>2002</u>	<u>2001</u>
Expected life	3 months	3 months	3 months
Risk-free interest rate	1.1-1.7%	1.8-3.6%	4.5-6.3%
Volatility	47%-77%	55%-70%	101%-139%
Dividend yield	0.00%	0.00%	0.00%

**PRO FORMA NET INCOME AND EARNINGS PER SHARE**

Had the Company amortized to expense the computed fair values of the 2003, 2002 and 2001 awards under the 1990 Stock Option Plan, 1995 Stock Option Plan, 2001 Stock Option Plan, 2000 Employee Stock Purchase Plan, and 1997 Employee Stock Purchase Plan, the Company’s pro forma net income (loss) and earnings (loss) per share for the three fiscal years in the period ended June 30, 2003 would have been as follows (in thousands, except per share amounts):

	<u>2003</u>	<u>2002</u>	<u>2001</u>
Net income (loss) as reported	(\$4,327)	(\$5,267)	\$17,359
Less: Total stock-based employee compensation determined under fair value based method for all awards, net of related taxes	(6,219)	(7,652)	(6,429)
Pro forma net income (loss)	<u>(\$10,546)</u>	<u>(\$12,919)</u>	<u>\$10,930</u>
Earnings (loss) per share:			
Basic – as reported	(\$0.17)	(\$0.21)	\$0.70
Basic – proforma	(\$0.41)	(\$0.51)	\$0.44
Diluted – as reported	(\$0.17)	(\$0.21)	\$0.64
Diluted – proforma	(\$0.41)	(\$0.51)	\$0.40

**REVENUE RECOGNITION** -- Revenue from product sales is recognized upon shipment provided title and risk of loss has passed to the customer, when persuasive evidence of an arrangement exists, the fee is fixed and determinable and collectibility is probable. Estimated costs for sales returns, price protection, stock rotation and

other allowances are accrued as a reduction of revenue in the period that sales are recognized. Domestic distributors are permitted a return allowance of up to 10% of their net purchases every six months.

**ADVERTISING COSTS** --- Advertising costs consist of development and placement costs of our advertising campaigns and are charged to expense when incurred. Advertising expense was \$8,000, \$20,000 and \$225,000 for fiscal years 2003, 2002 and 2001, respectively.

**CONCENTRATION OF CREDIT RISK AND CERTAIN SIGNIFICANT RISKS AND UNCERTAINTIES --**

- The Company sells its products primarily to a relatively small number of companies and generally does not require its customers to provide collateral or other security to support accounts receivable. The Company maintains allowances for estimated bad debt losses. The Company also purchases substantially all of its wafers from three suppliers and purchases other manufacturing services from a relatively small number of suppliers.

The Company participates in a dynamic high technology industry and believes that changes in any of the following areas could have a material adverse effect on the Company's future financial position or results of operations: advances and trends in new technologies; competitive pressures in the form of new products or price reductions on current products; changes in product mix; changes in the overall demand for products and services offered by the Company; changes in customer relationships; litigation or claims against the Company based on intellectual property, patent, product, regulatory or other factors; risks associated with having a concentration of a few suppliers; risks associated with changes in domestic and international economic and/or political conditions or regulations; availability of necessary components; and the Company's ability to attract and retain employees necessary to support its growth.

**COMPREHENSIVE INCOME** -- In fiscal year 1999, the Company adopted Statement of Financial Accounting Standards (SFAS) No. 130, Reporting Comprehensive Income, which requires an enterprise to report, by major components and as a single total, the change in net assets during the period from nonowner sources. Comprehensive income for the years ended June 30, 2003, 2002 and 2001 has been disclosed within the statement of shareholders' equity and comprehensive income.

**RECENTLY ISSUED ACCOUNTING STANDARDS** -- In June 2001, the FASB issued SFAS No. 142, "Goodwill and Other Intangible Assets". SFAS No. 142 addresses the initial recognition and measurement of intangible assets acquired outside of a business combination and the accounting for goodwill and other intangible assets subsequent to their acquisition. SFAS No. 142 provides that intangible assets with finite useful lives be amortized and that goodwill and intangible assets with indefinite lives will not be amortized, but will rather be tested at least annually for impairment. Pericom adopted SFAS No. 142 for its fiscal year beginning July 1, 2001. On that date, the Company ceased amortizing goodwill with a net carrying value of \$1,323,000 at June 30, 2001 and annual amortization of \$338,000 that resulted from business combinations initiated prior to the adoption of SFAS 141, Business Combinations. The Company determined that no impairment of goodwill existed in fiscal 2003. The Company will continue to evaluate such goodwill at least annually.

	<b>Years Ended June 30,</b>		
	<b>(In thousands, except per share amounts)</b>		
	<b><u>2003</u></b>	<b><u>2002</u></b>	<b><u>2001</u></b>
Reported net income (loss)	(\$4,327)	(\$5,267)	\$17,359
Add: Goodwill amortization	---	---	331
Adjusted net income (loss)	<u>(\$4,327)</u>	<u>(\$5,267)</u>	<u>\$17,690</u>
Reported basic earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.70
Add: Goodwill amortization per basic share	---	---	.01
Adjusted basic earnings (loss) per share	<u>(\$0.17)</u>	<u>(\$0.21)</u>	<u>\$0.71</u>

Reported diluted earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.64
Add: Goodwill amortization per diluted share	---	---	.01
Adjusted diluted earnings (loss) per share	<u>(\$0.17)</u>	<u>(\$0.21)</u>	<u>\$0.65</u>

In August 2001, the FASB issued SFAS No. 144, "Accounting for the Impairment or Disposal of Long-Lived Assets". SFAS No. 144 addresses financial accounting and reporting for the impairment or disposal of long-lived assets. It superseded SFAS No. 121, "Accounting for the Impairment of Long-Lived Assets and for Long-Lived Assets to be Disposed of", and the accounting and reporting provisions of Accounting Principles Board No. 30, "Reporting the Results of Operations – Reporting the Effects of Disposal of a Segment of a Business and Extraordinary, Unusual and Infrequently Occurring Events and Transactions" for the disposal of a segment of a business. The Company adopted SFAS No. 144 on July 1, 2002. The adoption of SFAS No. 141 did not have an impact on the Company's financial position, results of operations, or liquidity.

In April 2002, the FASB issued SFAS No. 145, "Rescission of FASB Statement No. 4, 44, and 64, Amendment of FASB Statement No. 13, and Technical Corrections". SFAS 145 requires that any gains or losses on extinguishment of debt that were classified as an extraordinary item in prior periods that are not unusual in nature and infrequent in occurrence be reclassified to other income (expense), beginning in fiscal 2003, with early adoption encouraged. The Company adopted SFAS No. 145 on July 1, 2002. The adoption of SFAS No. 145 did not have an impact on the Company's financial position or results of operations.

In June 2002, the FASB issued SFAS No. 146, "Accounting for Costs Associated with Exit or Disposal Activities", which addresses accounting for restructuring and similar costs. SFAS No. 146 supersedes previous accounting guidance, principally Emerging Issues Task Force Issue No. 94-3. The Company was required to adopt the provisions of SFAS No. 146 for restructuring activities initiated after December 31, 2002. SFAS No. 146 requires that the liability for costs associated with an exit or disposal activity be recognized when the liability is incurred. Under Issue 94-3, a liability for an exit cost was recognized at the date of the Company's commitment to an exit plan. SFAS No. 146 also establishes that the liability should initially be measured and recorded at fair value. Accordingly, SFAS No. 146 may affect the timing of recognizing future restructuring costs as well as the amounts recognized. The Company adopted SFAS No. 146 on January 1, 2003. The adoption of SFAS No. 146 had no impact on the Company's financial position or results of operations.

In November 2002, the FASB issued FASB Interpretation No. (FIN) 45, "Guarantor's Accounting and Disclosure Requirements for Guarantees, Including Indirect Guarantees of Indebtedness of Others." FIN 45 requires that upon issuance of a guarantee, the guarantor must recognize a liability for the fair value of the obligation it assumes under that guarantee. The provisions for initial recognition and measurement are effective on a prospective basis for guarantees that are issued or modified after December 31, 2002, irrespective of a guarantor's year-end. The disclosure requirements of FIN 45 are effective for interim and annual periods ending after December 15, 2002, and are applicable to the Company's product warranty liability and other guarantees. The Company adopted the provisions of FIN 45 for guarantees issued or modified after December 31, 2002, and the adoption of FIN 45 did not have a material impact on the Company's results of operations or financial position.

In December 2002, the FASB issued SFAS 148, "Accounting for Stock-Based Compensation — Transition and Disclosure," which amends SFAS 123, "Accounting for Stock-Based Compensation." SFAS 148 provides alternative methods of transition for an entity that voluntarily changes to the fair value based method of accounting for stock-based employee compensation. It also amends the disclosure provisions of SFAS 123 to require prominent disclosure about the effects an entity's accounting policy decisions with respect to stock-based employee compensation on reported net income in both annual and interim periods. SFAS 148 is effective for annual periods ending after December 15, 2002 and interim periods beginning after December 15, 2002. The Company adopted the disclosure requirements of SFAS 148 effective March 31, 2003 and continues to account for stock based compensation under APB Opinion 25 and related Interpretations. The adoption of the disclosure

requirements of SFAS 148 did not have a material impact on the Company's results of operations or financial position.

In January 2003, the FASB issued FIN 46, "Consolidation of Variable Interest Entities." FIN 46 establishes accounting guidance for consolidation of a variable interest entity (VIE), formerly referred to as special purpose entities. FIN 46 applies to any business enterprise, both public and private, that has a controlling interest, contractual relationship or other business relationship with a VIE. FIN 46 provides guidance for determining when an entity (the Primary Beneficiary) should consolidate a VIE that functions to support the activities of the Primary Beneficiary. The Company has no contractual relationship or other business relationship with a VIE, and therefore the adoption will not have an effect on the Company's consolidated results of operations or financial position.

**RECLASSIFICATIONS** – Certain reclassifications have been made to conform prior year amounts to the current year's presentation.

**EARNINGS PER SHARE** -- Basic earnings per share is based upon the weighted average number of common shares outstanding. Diluted earnings per share reflects the potential dilution that could occur if securities or other contracts to issue common stock were exercised or converted into common stock.

Basic and diluted earnings per share for each of the three years in the period ended June 30, 2003 are as follows:

	<u>Years Ended June 30,</u>		
	<u>2003</u>	<u>2002</u>	<u>2001</u>
Net income (loss)	(\$4,327)	(\$5,267)	\$17,359
Computation of common shares outstanding – basic earnings (loss) per share:			
Weighted average common stock	25,721	25,339	24,914
Basic earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.70
Computation of common shares outstanding – diluted earnings (loss) per share:			
Weighted average common stock	25,721	25,339	24,914
Dilutive options using the treasury stock method	---	---	2,328
Shares used in computing diluted earnings (loss) per share	25,721	25,339	27,242
Diluted earnings (loss) per share	(\$0.17)	(\$0.21)	\$0.64

Options to purchase 5,552,412 and 5,648,057 share of Common Stock were outstanding as of June 30, 2003 and June 30, 2002, respectively, and were excluded from the computation of net loss per share because such options were anti-dilutive. Options to purchase 1,486,189 shares of Common Stock at prices ranging from \$22.03 to \$42.75 were outstanding as of June 30, 2001, but not included in the computation of diluted net income per share because the options' exercise prices were greater than the average market price of the common shares as of such dates and therefore, would be anti-dilutive under the treasury stock method.

## 2. INVENTORIES

Inventories consist of (in thousands):

	As of June 30,	
	2003	2002
Finished goods	\$2,025	\$2,523
Work-in-process	2,486	3,921
Raw materials	5,452	2,895
	<u>\$9,963</u>	<u>\$9,339</u>

## 3. PROPERTY AND EQUIPMENT

Property and equipment consist of (in thousands):

	As of June 30,	
	2003	2002
Machinery and equipment	\$11,718	\$12,602
Computer equipment and software	9,137	8,586
Furniture and fixtures	773	769
Leasehold improvements	1,102	1,087
Construction-in-progress	356	---
Total	<u>23,086</u>	<u>23,044</u>
Accumulated depreciation and amortization	<u>(16,781)</u>	<u>(14,436)</u>
Property and equipment – net	<u>\$6,305</u>	<u>\$8,608</u>

Construction-in-progress is primarily leasehold improvements and machinery and equipment that have not been placed in service.

## 4. JOINT VENTURE AND OTHER INVESTMENTS

In fiscal 1994, the Company purchased 1,500,000 shares of Series A Convertible Preferred Stock issued by Pericom Technology, Inc. ("PTI") for \$750,000 (an 18.4% equity investment at the time). Such preferred stock is convertible at the option of the Company into 1,500,000 shares of PTI common stock, does not bear dividends, has a liquidation preference up to the purchase price and votes based on the number of common shares into which it is convertible. In fiscal 2000, the Company purchased an additional 909,090 shares of Series B Convertible Preferred Stock for \$1 million and converted \$3.5 million in debt to purchase an additional 3,181,818 shares of Series B Convertible Preferred Stock. The Series B issue has liquidation preference over the Series A issue and all common shares. With the fiscal 2000 additional investment, the Company had a 43.3% equity investment in PTI. After the additional investment there was approximately a \$1.7 million difference between the carrying value and the underlying equity in net assets of the investment which was being amortized over five years. As a result of the adoption of SFAS No. 142, the last month of amortization was June of 2001. In fiscal 2001, the Company purchased an additional 3,000,000 shares of Series C Convertible Preferred Stock for approximately \$5 million. The Series C issue has liquidation preference over all other classes of stock. With the fiscal 2001 additional investment, the Company had a 45.0% equity investment in PTI. As of June 28, 2003 the Company has a 44.1% equity investment in PTI. The investment in PTI is accounted for using the equity method due to the Company's significant influence over its operations. In addition, several of the directors of the Company are also directors of PTI, and certain shareholders of the Company are also shareholders of PTI. PTI was incorporated in 1994 and in 1995 established a design center and sales office to pursue opportunities and participate in joint ventures in China. During the years ended June 30, 2003 and 2002, the Company sold \$142,000 and \$66,000 respectively, in services to PTI. During the year ended June 30, 2001, the Company did not sell any services to PTI. During the years ended June 30, 2003, 2002 and 2001, the Company purchased \$557,000, \$689,000 and \$1,300,000 in services from PTI, respectively. At June 30, 2003, 2002 and 2001, \$152,000, \$98,000 and \$134,000 respectively, was owed to the Company by PTI for reimbursement of certain administrative expenses incurred by the Company on behalf of PTI and for

advances made to PTI by the Company. Condensed financial information of the joint venture at June 30, 2003 and June 30, 2002 is as follows (in thousands):

	<u>2003</u>	<u>2002</u>
Total assets	\$12,865	\$12,768
Total liabilities	\$1,235	\$1,137
Total equity	\$11,630	\$11,631
Revenue	\$5,740	\$3,515
Cost of revenues	3,614	2,732
Gross profit	<u>2,126</u>	<u>783</u>
Expenses	3,435	2,972
Operating Loss	<u>(1,309)</u>	<u>(2,189)</u>
Interest and other income	343	282
Income tax provision	0	21
Net loss	<u><u>(\$966)</u></u>	<u><u>(\$1,928)</u></u>

The Company has investments in certain privately held companies, which it accounts for under the cost method. The Company has invested approximately \$7.0 million in Saronix LLC as of June 30, 2003. The investment is in the form of a loan agreement and other associated costs. The loan bears interest of 8%. This rate will be adjusted upward uniformly with changes in the prime rate but will not be reduced. The loan is secured by physical and intangible assets of Saronix, LLC as well as a product and technology license agreement. In addition, the Company entered into a Purchase Option Agreement whereby the Company may acquire certain assets and assume certain liabilities of Saronix, LLC for the amounts previously provided under the loan agreement. The purchase option expires on October 15, 2003 (see Note 15). The carrying amount of other such investments was approximately \$3.0 million and \$2.7 million at June 30, 2003 and 2002, respectively, and these amounts are recorded in other assets. The Company recorded write offs of certain investments totaling approximately \$1.2 million in 2003 and approximately \$2.7 million in 2002, as the Company determined that these investments had experienced an other than temporary decline in value.

## 5. ACCRUED LIABILITIES

Accrued liabilities consist of (in thousands):

	<u>As of June 30,</u>	
	<u>2003</u>	<u>2002</u>
Accrued compensation	\$ 1,240	\$ 1,132
External sales representative commissions	632	727
Restructuring reserve – current portion	653	197
Other accrued expenses	600	784
	<u>\$ 3,125</u>	<u>\$ 2,840</u>

## 6. SHAREHOLDERS' EQUITY

### *PREFERRED STOCK*

The number of shares of preferred stock authorized to be issued is 5,000,000. The Board of Directors is authorized to issue the preferred stock from time to time in one or more series and to fix the rights, privileges and restrictions of the shares of such series. As of June 30, 2003, no shares of preferred stock were outstanding.

## STOCK OPTION PLANS

Under the Company's 2001, 1995, and 1990 Stock Option Plans, incentive and nonqualified stock options to purchase up to 6,273,042 shares of common stock have been reserved at June 30, 2003 for issuance to employees, officers, directors, independent contractors and consultants of the Company.

The options may be granted at not less than the fair value and not less than 85% of the fair value on grant date for incentive stock options and nonqualified stock options, respectively. Options vest over periods of up to 48 months as determined by the Board of Directors. Options granted under the Plans expire 10 years from grant date.

Activity in the Company's option plans is summarized below:

	<u>Shares</u>	<u>Weighted Average Exercise Price</u>
Balance, June 30, 2000 (1,886,648 exercisable at a weighted average price of \$2.38)	4,526,864	\$7.76
Granted (weighted average fair value of \$18.91 per share)	1,897,555	22.62
Exercised	(534,216)	2.92
Canceled	<u>(1,143,702)</u>	24.22
Balance, June 30, 2001 (2,397,294 exercisable at a weighted average price of \$5.24)	4,746,501	10.28
Granted (weighted average fair value of \$7.83 per share)	1,553,255	13.49
Exercised	(423,609)	1.74
Canceled	<u>(228,090)</u>	15.95
Balance, June 30, 2002 (3,256,939 exercisable at a weighted average price of \$9.00)	5,648,057	11.57
Granted (weighted average fair value of \$4.09 per share)	576,300	8.60
Exercised	(241,839)	2.64
Canceled	<u>(430,106)</u>	15.33
Balance, June 30, 2003	<u>5,552,412</u>	\$11.36

At June 30, 2003, 720,630 shares were available for future issuance under the option plans.

Additional information regarding options outstanding as of June 30, 2003 is as follows:

<u>Range of Exercise Prices</u>	<u>Options Outstanding</u>			<u>Options Exercisable</u>	
	<u>Number Outstanding</u>	<u>Weighted Average Remaining Contractual Life (Years)</u>	<u>Weighted Average Exercise Price</u>	<u>Number Exercisable</u>	<u>Weighted Average Exercise Price</u>
\$0.50 – 2.40	1,151,159	3.65	\$1.86	1,151,159	\$1.86
\$2.44 – 8.70	1,362,748	7.12	6.21	902,510	5.03
\$8.74 – 14.20	1,541,317	8.22	12.74	729,248	12.98
\$14.40 – 25.63	1,114,443	7.49	18.93	767,788	19.62
\$25.75 – 39.03	382,745	7.04	30.74	292,696	30.65
\$0.05 – 39.03	<u>5,552,412</u>	6.78	\$11.36	<u>3,843,401</u>	\$10.45

On April 6, 2001, the Company announced a voluntary stock option exchange program for our employees. Under the program, employees were given the opportunity to elect to cancel outstanding stock options held by them with an exercise price of \$15.00 or more per share in exchange for an equal number of replacement options to be granted at a future date not less than six months and a day after the options were canceled. Board members were not eligible. The elections to cancel options were effective on May 4, 2001. The exchange resulted in the voluntary cancellation by 60 employees of 611,405 employee stock options with exercise prices ranging from \$18.50 to \$42.75 in exchange for the same number of replacement options. The replacement options were granted on November 5, 2001. The replacement options have the same terms and conditions as each optionee's cancelled options, including the expiration date of the cancelled options, except that: (1) 12.5% of the replacement options vested on the grant date and the remainder will vest at a rate of 1/48 per month for 42 months, (2) the replacement options have an exercise price equal to the fair market value of our common stock on the date of the grant and (3) the optionee must be an employee of the Company on the date of grant of the replacement options in order to receive replacement options.

In December 2001, the Company recorded of a stock compensation charge of \$253,000 related to certain stock options that were allowed an extended vesting period from that which was allowed in the original option agreement in conjunction with a reduction in force, which the Company undertook in that quarter. The offset of this charge was to shareholders' equity.

#### ***1997 EMPLOYEE STOCK PURCHASE PLAN***

In 1997, the Company approved the 1997 Employee Stock Purchase Plan (the "Stock Purchase Plan"), which allows eligible employees of the Company to purchase shares of Common Stock through payroll deductions. A total of 600,000 shares of the Company's Common Stock was reserved for issuance under the Stock Purchase Plan. The Stock Purchase Plan permits eligible employees to purchase Common Stock at a discount through payroll deductions, during 24-month purchase periods, except that the first purchase period of the plan will be 27 months. Each purchase period will be divided into eight consecutive three-month accrual periods, except that the first accrual period will be six months. The price at which stock is purchased under the Stock Purchase Plan is equal to 85% of the fair market value of the Common Stock on the first day of the purchase period or the last day of the accrual period, whichever is lower. The initial purchase period commenced upon the effective date of the Company's initial public offering of Common Stock in October 1997 and ended on January 30, 2000. The maximum number of shares of Common Stock that any employee may purchase under the Stock Purchase Plan during any accrual period is 1,000 shares. During fiscal year 2001 and 2000, the Company issued 80,223 and 219,358 shares of common stock under the Stock Purchase Plan at weighted average prices of \$5.42 and \$2.63, respectively. The weighted average fair value of the fiscal 2001 and 2000 awards for each year was \$9.66 and \$2.21 per share. The last issuance under the 1997 Employee Stock Purchase Plan was on July 31, 2000.

#### ***2000 EMPLOYEE STOCK PURCHASE PLAN***

In 2000, the Company approved the 2000 Employee Stock Purchase Plan (the "Stock Purchase Plan"), which allows eligible employees of the Company to purchase shares of Common Stock through payroll deductions. A total of 600,000 shares of the Company's Common Stock has been reserved for issuance under the Stock Purchase Plan. The Stock Purchase Plan permits eligible employees to purchase Common Stock at a discount through payroll deductions, during 24-month purchase periods. Each purchase period will be divided into eight consecutive three-month accrual periods. The price at which stock is purchased under the Stock Purchase Plan is equal to 85% of the fair market value of the Common Stock on the first day of the purchase period or the last day of the accrual period, whichever is lower. The maximum number of shares of Common Stock that any employee may purchase under the Stock Purchase Plan during any accrual period is 1,000 shares. During fiscal year 2003 and 2002, the Company issued 137,629 and 95,498, shares of common stock under the Stock Purchase Plan at weighted average prices of \$7.61 and \$11.77, respectively. The weighted average fair value of the fiscal 2003 and fiscal 2002 awards for each year was \$4.09 and \$7.83 per share.

## ISSUANCE OF STOCK OPTIONS TO NON-EMPLOYEES

In 2001, the Company issued nonstatutory options to a non-employee for the purchase of 20,000 shares of common stock at an exercise price of \$18.19. These options vest over four years. In accordance with SFAS No. 123, its related interpretations and EITF 96-18, the Company accounted for this award under the fair value method and as a variable award. Accordingly, the Company records stock compensation equal to the fair value of the options and the vesting schedule (using the Black-Scholes option pricing model and the following assumptions: contractual life of ten years; risk free interest rate of 5.35%; volatility of 91%; and dividend yield of zero) and remeasures the compensation expense at the end of each period until the award vests. The related compensation expense of \$137,000 was recognized as selling, general and administrative expense in the accompanying statement of operations in 2001. The non-employee became an employee in 2002 and the unvested options are accounted for in accordance with APB No. 25. No options were issued to non-employees during the years ended June 30, 2002 or June 30, 2003.

## 7. INCOME TAXES

The provision for income taxes consists of (in thousands):

	<u>Fiscal Year Ended June 30,</u>		
	<u>2003</u>	<u>2002</u>	<u>2001</u>
Federal:			
Current	(\$2,133)	(\$2,682)	\$5,863
Deferred	(536)	(230)	585
	<u>(2,669)</u>	<u>(2,912)</u>	<u>6,448</u>
State:			
Current	22	4	744
Deferred	(928)	(1,048)	110
	<u>(906)</u>	<u>(1,044)</u>	<u>854</u>
Foreign:			
Current	9	---	---
Deferred	229	(229)	---
	<u>238</u>	<u>(229)</u>	<u>---</u>
Charge in lieu of taxes attributable to employee stock plans	570	1,223	2,487
Provision for income taxes	<u>(\$2,767)</u>	<u>(\$2,962)</u>	<u>\$9,789</u>

A reconciliation between the Company's effective tax rate and the U.S. statutory rate is as follows:

	<u>Fiscal Year Ended June 30,</u>		
	<u>2003</u>	<u>2002</u>	<u>2001</u>
Tax at federal statutory rate	35.0%	35.0%	35.0%
State income taxes, net of federal benefit	8.3	8.1	2.1
Tax exempt investment income	1.1	2.4	(1.8)
Research and development tax credits	3.5	6.2	(2.3)
Change in valuation allowance	(8.9)	(15.1)	---
Other	---	(0.6)	3.1
Provision for income taxes	<u>39.0%</u>	<u>36.0%</u>	<u>36.1%</u>

The components of the net deferred tax assets were as follows (in thousands):

	<u>As of June 30,</u>	
	<u>2003</u>	<u>2002</u>
Deferred tax assets:		
Accruals and reserves recognized in different periods	\$2,578	\$1,967
NOL and credit carryforwards	987	624
Tax effect of other than temporary decline in the value of investments	830	1,081
Tax effect of accumulated losses of investment in joint venture	1,375	1,291
Other	679	111
	<hr/> 6,449	<hr/> 5,074
Deferred tax liabilities:		
Basis difference in fixed assets	(747)	(1,146)
Tax effect of unrealized gain on short-term investments	(710)	---
Other	(86)	(100)
	<hr/> (1,543)	<hr/> (1,246)
Less valuation allowance	(2,420)	(1,867)
Net deferred tax assets	<hr/> <hr/> \$2,486	<hr/> <hr/> \$1,961

As of June 30, 2003, the Company has tax credit carryforwards of approximately \$1,108,000 available to offset future state taxable income. The state tax credit carryforwards do not have an expiration date and may be carried forward indefinitely.

The Company provides a valuation allowance for deferred tax assets when it is more likely than not, based upon currently available evidence and other factors, that some portion or all of the deferred tax asset will not be realized. Due to the uncertainty surrounding the Company's ability to offset the cumulative equity losses in Pericom Technology, Inc. ("PTI") and writedowns in investments that took place during the year, the Company has provided for a partial valuation allowance in the amount of approximately \$2,420,000 against the tax effect of these losses as of year-end. However, the valuation allowance has been reduced by the tax effect of any realized and unrealized capital gains that are available to offset such losses as of year-end.

## 8. LEASES

The Company leases certain facilities under operating leases with termination dates on or before May 2006. Generally, these leases have at least one option to extend for an additional three years upon termination of the original lease term. The future minimum operating lease commitments at June 30, 2003 are as follows (in thousands):

Fiscal Year:	
2004	\$1,752
2005	771
2006	111
	<hr/> \$2,634

Rent expense for the years ended June 30, 2003, 2002 and 2001 was \$1,486,000, \$1,468,000 and \$1,322,000, respectively. This is net of sublease proceeds received of approximately \$241,000 and \$237,000 for the years ended June 30, 2003 and June 30, 2002, respectively.

## 9. CONTINGENCIES

The semiconductor industry is characterized by frequent claims and related litigation regarding patent and other intellectual property rights. The Company settled an outstanding claim of this nature in fiscal 2002 without

material adverse effect on the Company's financial position, results of operations or cash flows. There were no such claims or settlements in fiscal 2003.

## 10. INDUSTRY AND SEGMENT INFORMATION

In fiscal year 1999, the Company adopted SFAS No. 131, Disclosures about Segments of an Enterprise and Related Information, which establishes annual and interim reporting standards for an enterprise's business segments and related disclosures about its products, services, geographical areas and major customers. The Company operates in one reportable segment.

In fiscal 2003 one end user customer who purchases through the contract manufacturing channel represented 11% of net revenues, and one distributor and one contract manufacturer each of whom purchase directly from the Company and sell to multiple end user customers, each represented 12% of net revenues. At June 30, 2003, two customers each represented 10% of trade accounts receivable. In fiscal 2002 three end user customers who purchase through distribution and contract manufacturing channels represented 15%, 12% and 11%, respectively, of net revenues. At June 30, 2002, two customers each represented 10% of trade accounts receivable. In fiscal 2001 one end user customer who purchases through distribution and contract manufacturing channels represented 12% of net revenues, one end user customer who purchases directly from the Company and through the contract manufacturing channel represented 11% of net revenues and one distributor who sells to multiple end customers represented 10% of net revenues. At June 30, 2001, one direct customer represented 13% of trade accounts receivable.

For geographical reporting, net sales are attributed to the region where customers are located. Long-lived assets consist of property and equipment and are attributed to the region where they are located. The following presents net sales for the years ended June 30, 2003, 2002 and 2001; and long-lived assets as of June 30, 2003, 2002 and 2001 (in thousands):

	<u>Fiscal Year Ended June 30,</u>		
	<u>2003</u>	<u>2002</u>	<u>2001</u>
Net sales to geographic regions:			
North America	\$14,351	\$15,817	\$54,850
Europe	3,742	3,982	12,228
Asia	26,865	27,790	41,235
Total net sales	\$44,958	\$47,589	\$108,313
Long-lived assets:			
North America	\$4,488	\$6,267	\$7,761
Asia	1,811	2,335	2,702
Other (less than 10%)	6	6	10
Total long-lived assets	<u>\$6,305</u>	<u>\$8,608</u>	<u>\$10,473</u>

## 11. EMPLOYEE BENEFIT PLAN

The Company has a 401(k) tax-deferred savings plan under which eligible employees may elect to have a portion of their salary deferred and contributed to the plan. Employer matching contributions are determined by the Board of Directors and are discretionary. There were no employer matching contributions in fiscal 2003, 2002 or 2001.

## 12. RESTRUCTURING CHARGE

In fiscal 2003 there was a restructuring charge of \$1,431,000 related to an unused leased facility. The restructuring charge is expected to be fully depleted in the fourth quarter of fiscal 2005, when the lease terminates. On June 30, 2003, the remaining balance of the restructuring charge was \$1,272,000. In fiscal 2001

there was a restructuring charge of \$522,000 related to the same unused leased facility. On June 30, 2002 and June 30, 2001, the remaining balance of this earlier restructuring charge was \$522,000 and \$197,000, respectively. The restructuring charge that was booked in fiscal 2001 was fully depleted in the fourth quarter of fiscal 2003. There were no restructuring charges in fiscal 2002.

### 13. SHAREHOLDER RIGHTS PLAN

Pericom has adopted a Shareholder Rights Plan and declared a dividend distribution of one right for each outstanding share of the Company's common stock. The record date for the distribution was March 21, 2002. The plan is designed to protect the long-term value of the Company for its shareholders during any future unsolicited acquisition attempt. Adoption of the plan was not made in response to any specific attempt to acquire the Company or its shares and the Company is not aware of any current efforts to do so. These rights will become exercisable only upon the occurrence of certain events specified in the plan, including the acquisition of 15% of the Company's outstanding common stock by a person or group. After a person or group acquires 15% or more of the outstanding common stock or announces a tender offer, the consummation of which would result in ownership by a person or group of 15% or more of the outstanding common stock, the rights will become exercisable by persons other than the acquiring person, unless the Board of Directors has approved the transaction in advance. Each right entitles the holder, other than an acquiring person, to acquire shares of the Company's common stock (or, in the event that there are insufficient authorized common stock shares, substitute consideration such as cash, property, or other securities of the Company, such as Preferred Stock) at a 50% discount to the then prevailing market price. Prior to the acquisition by a person or group of 15% or more of the outstanding common stock, the rights are redeemable for \$0.001 per right at the option of the Board of Directors. The rights will expire on March 6, 2012. As of June 30, 2003, there were 25,765,177 rights outstanding.

### 14. OTHER ACCOUNTS RECEIVABLE

Other accounts receivable consist of (in thousands):

	<u>As of June 30,</u>	
	<u>2003</u>	<u>2002</u>
Interest receivable	\$ 1,578	\$ 1,771
Other accounts receivable	222	100
	<u>\$ 1,800</u>	<u>\$ 1,871</u>

### 15. SUBSEQUENT EVENTS

In July 2003, the Company loaned an additional \$497,000 to Saronix LLC. On September 24, 2003 the Company exercised its option to acquire certain assets and certain liabilities of Saronix LLC. The purchase price will be equal to the amounts invested. Saronix is a frequency timing products company with a complementary product portfolio. It is anticipated that the purchase will be completed on October 1, 2003.

**PERICOM SEMICONDUCTOR CORPORATION**

**QUARTERLY FINANCIAL DATA**

(Amounts in thousands, except per share data)  
(Unaudited)

	<u>Three Months Ended</u>							
	<u>June 30</u> <u>2003</u>	<u>Mar 31</u> <u>2003</u>	<u>Dec 31</u> <u>2002</u>	<u>Sep 30</u> <u>2002</u>	<u>June 30</u> <u>2002</u>	<u>Mar 31</u> <u>2002</u>	<u>Dec 31</u> <u>2001</u>	<u>Sep 30</u> <u>2001</u>
Net revenues	\$11,506	\$11,492	\$11,132	\$10,828	\$12,967	\$12,546	\$8,962	\$13,114
Cost of revenues	8,027	8,029	7,852	7,560	8,294	7,926	9,018	8,633
Gross profit (loss)	3,479	3,463	3,280	3,268	4,673	4,620	(56)	4,481
Operating expenses:								
Research and development	2,871	2,758	2,800	2,918	2,958	2,966	3,160	2,579
Selling, general and administrative	2,653	2,673	2,799	3,158	2,949	2,701	3,185	2,943
Restructuring charge	---	---	1,431	---	---	---	---	---
Total operating expenses	5,524	5,431	7,030	6,076	5,907	5,667	6,345	5,522
Income (loss) from operations	(2,045)	(1,968)	(3,750)	(2,808)	(1,234)	(1,047)	(6,401)	(1,041)
Other income (loss), net	1,438	1,023	(84)	1,100	152	(922)	1,113	1,151
Income (loss) before income taxes	(607)	(945)	(3,834)	(1,708)	(1,082)	(1,969)	(5,288)	110
Income tax provision (benefit)	(237)	(313)	(1,431)	(786)	(388)	(709)	(1,904)	39
Net income (loss)	(\$370)	(\$632)	(\$2,403)	(\$922)	(\$694)	(\$1,260)	(\$3,384)	\$71
Basic earnings (loss) per share	(\$0.01)	(\$0.03)	(\$0.09)	(\$0.04)	(\$0.03)	(\$0.05)	(\$0.13)	\$0.00
Diluted earnings (loss) per share	(\$0.01)	(\$0.03)	(\$0.09)	(\$0.04)	(\$0.03)	(\$0.05)	(\$0.13)	\$0.00
Shares used in computing basic earnings (loss) Per share	25,705	25,717	25,730	25,730	25,530	25,394	25,242	25,189
Share used in computing diluted earnings (loss) Per share	25,705	25,717	25,730	25,730	25,530	25,394	25,242	27,114

During the three months ended December 31, 2001, the Company recorded a charge of approximately \$3.2 million against revenue to address inventory and pricing issues in our distribution channel that were due to the significant drop-off in business activity; additional inventory reserves of approximately \$534,000 related to a workforce reduction and legal expenses associated with a patent infringement suit initiated by a competitor.

During the three months ended March 31, 2002, the Company wrote off a loan and accrued interest receivable of approximately \$1.8 million as the Company determined that this loan was uncollectible. During the three months ended June 30, 2002, the Company wrote off an investment of approximately \$900,000 as the Company determined the investment had experienced an other than temporary decline in value.

During the three months ended December 31, 2002, the Company recorded a restructuring charge of approximately \$1.4 million related to an unused leased facility; and wrote off in its entirety an investment in another company of approximately \$1.1 million that was deemed to be impaired.



Schedule II

PERICOM SEMICONDUCTOR CORPORATION

VALUATION AND QUALIFYING ACCOUNTS  
(In Thousands)

	<u>Balance at Beginning Of Period</u>	<u>Charged to Revenues</u> (a)	<u>Deductions</u> (b)	<u>Balance at End of Period</u>
Reserves for returns and pricing adjustments				
June 30,				
2003	\$4,276	\$7,618	\$8,448	\$3,446
2002	4,146	14,299	14,169	4,276
2001	3,243	17,009	16,106	4,146

(a) Primarily related to sales reserves

(b) Charges for which allowances were created

	<u>Balance at Beginning Of Period</u>	<u>Charged to Costs and Expenses</u> (c)	<u>Deductions</u> (d)	<u>Balance at End of Period</u>
Allowance for doubtful accounts				
June 30,				
2003	\$100	\$ ---	\$ ---	\$100
2002	150	---	50	100
2001	100	50	---	150

(c) Related to identified uncollectible accounts

(d) Charges for which allowances were created

**PERICOM SEMICONDUCTOR CORPORATION  
CERTIFICATION PURSUANT TO SECTION 302 OF THE  
SARBANES-OXLEY ACT OF 2002**

I, Alex C. Hui, Chief Executive Officer of Pericom Semiconductor Corporation, certify that:

1. I have reviewed this annual report on Form 10-K of Pericom Semiconductor Corporation;
2. Based on my knowledge, this report does not contain any untrue statement of a material fact or omit to state a material fact necessary to make the statements made, in light of the circumstances under which such statements were made, not misleading with respect to the period covered by this report;
3. Based on my knowledge, the financial statements, and other financial information included in this report, fairly present in all material respects the financial condition, results of operations and cash flows of the registrant as of, and for, the periods presented in this report;
4. The registrant's other certifying officers and I are responsible for establishing and maintaining disclosure controls and procedures (as defined in Exchange Act Rules 13a-15(e) and 15d-15(e)) for the registrant and have:
  - a) Designed such disclosure controls and procedures, or caused such disclosure controls and procedures to be designed under our supervision, to ensure that material information relating to the registrant, including its consolidated subsidiaries, is made known to us by others within those entities, particularly during the period in which this report is being prepared;
  - b) Evaluated the effectiveness of the registrant's disclosure controls and procedures and presented in this report our conclusions about the effectiveness of the disclosure controls and procedures, as of the end of the period covered by this report based on such evaluation; and
  - c) Disclosed in this report any change in the registrant's internal control over financial reporting that occurred during the registrant's most recent fiscal quarter (the registrant's fourth fiscal quarter in the case of an annual report) that has materially affected, or is reasonably likely to materially affect, the registrant's internal control over financial reporting; and
5. The registrant's other certifying officer(s) and I have disclosed, based on our most recent evaluation of internal control over financial reporting, to the registrant's

auditors and the audit committee of the registrant's board of directors (or persons performing the equivalent functions):

- a) All significant deficiencies and material weaknesses in the design or operation of internal control over financial reporting which are reasonably likely to adversely affect the registrant's ability to record, process, summarize and report financial data; and
- b) Any fraud, whether or not material, that involves management or other employees who have a significant role in the registrant's internal control over financial reporting.

Date: September 26, 2003

/s/ Alex C. Hui  
Alex C. Hui  
Chief Executive Officer  
Pericom Semiconductor Corporation

**PERICOM SEMICONDUCTOR CORPORATION  
CERTIFICATION PURSUANT TO SECTION 302 OF THE  
SARBANES-OXLEY ACT OF 2002**

I, Michael D. Craighead, Chief Financial Officer of Pericom Semiconductor Corporation, certify that:

1. I have reviewed this annual report on Form 10-K of Pericom Semiconductor Corporation;
2. Based on my knowledge, this report does not contain any untrue statement of a material fact or omit to state a material fact necessary to make the statements made, in light of the circumstances under which such statements were made, not misleading with respect to the period covered by this report;
3. Based on my knowledge, the financial statements, and other financial information included in this report, fairly present in all material respects the financial condition, results of operations and cash flows of the registrant as of, and for, the periods presented in this report;
4. The registrant's other certifying officers and I are responsible for establishing and maintaining disclosure controls and procedures (as defined in Exchange Act Rules 13a-15(e) and 15d-15(e)) for the registrant and have:
  - a) Designed such disclosure controls and procedures, or caused such disclosure controls and procedures to be designed under our supervision, to ensure that material information relating to the registrant, including its consolidated subsidiaries, is made known to us by others within those entities, particularly during the period in which this report is being prepared;
  - b) Evaluated the effectiveness of the registrant's disclosure controls and procedures and presented in this report our conclusions about the effectiveness of the disclosure controls and procedures, as of the end of the period covered by this report based on such evaluation; and
  - c) Disclosed in this report any change in the registrant's internal control over financial reporting that occurred during the registrant's most recent fiscal quarter (the registrant's fourth fiscal quarter in the case of an annual report) that has materially affected, or is reasonably likely to materially affect, the registrant's internal control over financial reporting; and
5. The registrant's other certifying officer(s) and I have disclosed, based on our most recent evaluation of internal control over financial reporting, to the registrant's

auditors and the audit committee of the registrant's board of directors (or persons performing the equivalent functions):

- a) All significant deficiencies and material weaknesses in the design or operation of internal control over financial reporting which are reasonably likely to adversely affect the registrant's ability to record, process, summarize and report financial data; and
- b) Any fraud, whether or not material, that involves management or other employees who have a significant role in the registrant's internal control over financial reporting.

Date: September 26, 2003

/s/ Michael D. Craighead

Michael D. Craighead  
Chief Financial Officer  
Pericom Semiconductor Corporation

PERICOM SEMICONDUCTOR CORPORATION

CERTIFICATION OF CHIEF EXECUTIVE OFFICER

PURSUANT TO 18 U.S.C. SECTION 1350 AS ADOPTED

PURSUANT TO SECTION 906 OF THE SARBANES-OXLEY ACT OF 2002

In connection with this annual report of Pericom Semiconductor Corporation (the "Company") on Form 10-K for the year ended June 29, 2003 (the "Report"), I, Alex C. Hui, Chief Executive Officer of the Company, hereby certify as of the date hereof, solely for purposes of Title 18, Chapter 63, Section 1350 of the United States Code, that to the best of my knowledge:

(1) the Report fully complies with the requirements of Section 13(a) or 15(d), as applicable, of the Securities Exchange Act of 1934, and

(2) the information contained in the Report fairly presents, in all material respects, the financial condition and results of operations of the Company at the dates and for the periods indicated.

September 26, 2003

By: /s/ Alex C. Hui  
Alex C. Hui  
Chief Executive Officer  
Pericom Semiconductor Corporation

PERICOM SEMICONDUCTOR CORPORATION

CERTIFICATION OF CHIEF EXECUTIVE OFFICER

PURSUANT TO 18 U.S.C. SECTION 1350 AS ADOPTED

PURSUANT TO SECTION 906 OF THE SARBANES-OXLEY ACT OF 2002

In connection with this annual report of Pericom Semiconductor Corporation (the "Company") on Form 10-K for the year ended June 29, 2003 (the "Report"), I, Michael D. Craighead, Chief Financial Officer of the Company, hereby certify as of the date hereof, solely for purposes of Title 18, Chapter 63, Section 1350 of the United States Code, that to the best of my knowledge:

- (1) the Report fully complies with the requirements of Section 13(a) or 15(d), as applicable, of the Securities Exchange Act of 1934, and
- (2) the information contained in the Report fairly presents, in all material respects, the financial condition and results of operations of the Company at the dates and for the periods indicated.

September 26, 2003

By: /s/ Michael D. Craighead  
Michael D. Craighead  
Chief Financial Officer  
Pericom Semiconductor Corporation

**This page intentionally left blank.**

**This page intentionally left blank.**

# Pericom 2003 Company Information

## Board of Directors

Alex Chi-Ming Hui	.....• Chief Executive Officer, President, and Chairman of the Board of Directors
Chi-Hung (John) Hui, Ph.D.	.....• Vice President of Technology, Director
Hau L. Lee, Ph.D.* ~	.....• Director
Millard (Mel) Phelps * ~	.....• Director
Tay Thiam Song * °	.....• Director
Jeffrey Young ° ~	.....• Director

\* Member of Audit Committee  
° Member of Compensations Committee  
~ Member of Nomination Committee

## Executive Officers

Patrick B. Brennan	.....• Vice President of Investor Relations
Michael D. Craighead	.....• Vice President & Chief Financial Officer
Tat C. Choi, Ph.D.	.....• Vice President of Design Engineering
Gerald V. Beemiller	.....• Vice President of Sales
David K. Raun	.....• Vice President of Marketing
Shujong (John) Cheng	.....• Vice President of Operations
Michael Chen	.....• Vice President of ASIC Engineering, General Manager of Pericom Taiwan, LTD

### Legal Counsel

Morrison & Foerster LLP  
755 Page Mill Road  
Palo Alto, California 94304  
Phone: 650.813.5600

### Independent Accountants

Deloitte & Touche LLP  
225 W. Santa Clara Street, Ste. 600  
San Jose, California 95113  
Phone: 408.998.4000

### Registrar & Transfer Agent

EquiServe Trust Company, N.A.  
P.O. Box 43023  
Providence, Rhode Island 02940

## Annual Meeting

The annual meeting of shareholders for Pericom Semiconductor will be on Wednesday, December 17, 2003, at 3:00 p.m., Pacific Standard Time, on the company premises, 2380 Bering Drive, San Jose, California 95131.

## Common Stock

Pericom Semiconductor Corporation's Common Stock is traded on the NASDAQ National Market under the symbol "PSEM".

## Form 10-K

A copy of the Pericom Annual Report on Form 10-K as filed with the Securities and Exchange Commission will be made available without charge to all shareholders upon written request to the company. Requests should be directed to the attention of the Chief Financial Officer, Michael D. Craighead, at the corporate office on 2380 Bering Drive, San Jose, California 95131.

## Legal Matters

Questions regarding legal matters should be directed to the Chief Financial Officer, Michael D. Craighead.

complete interface solutions

[www.pericom.com](http://www.pericom.com)

memory module applications

analog switch

signal translation

server applications

LVDS

logic interface

crystal oscillator

gigabit-LAN switches

translators

world leader in digital bus switches

clock & timing solutions

AsSP switches

USB2.0 switches

SerDes

PCI-to-PCI bridges



Pericom Semiconductor Corporation  
2380 Bering Drive, San Jose, CA 95131  
Phone: 408.435.0800  
Fax: 408.435.1100  
[www.pericom.com](http://www.pericom.com)  
[solutions@pericom.com](mailto:solutions@pericom.com)

SKU PRI-AR-03